Project 'GAMERA'

(Semi-Powerful Console(Windows & Linux) Tools & gigabytes of English texts, downloadable from www.sanmayce.com)

WHERE THE WORD COUNTS



Caterpillar(LZSS-King of Brute-Force Heavy Sentence Dumpers, 32bit console application), revision 14+ Kazuya(LZ-Sovereign of Brute-Force Heavy Sentence Dumpers, 32bit console application), revision 17++ Salah-ed-din(GZ-Sultan of Brute-Force Heavy Sentence Dumpers, 32bit console application), revision 14++ Raccoondog(LZMA-Baron of Brute-Force Heavy Sentence Dumpers, 32bit console application), revision 17++ Yoshi(Filelist creator and more, 32bit console application), revision 6 Leprechaun(Fast and Greedy Word_Ripper, 32bit console application), revision 13++

WinRAR archive in eleven 624MB volumes • Required HDD space: 6.56 GB (ready to go when extracted on D:\) • 2010 JUN 06

Kazuya delivers english sentences at 85-255MB/s speed(Obtained with Toshiba Satellite L305 (Intel Pentium(Merom-1M) T3400 2.16GHz))
Salah-ed-din delivers english sentences at 114-117MB/s speed(Obtained with Toshiba Satellite L305 (Intel Pentium(Merom-1M) T3400 2.16GHz))
Raccoondog delivers english sentences at 39MB/s speed(Obtained with Toshiba Satellite L305 (Intel Pentium(Merom-1M) T3400 2.16GHz))
Leprechaun rips 6,142,696++ words per second(Obtained with Toshiba Satellite L305 (Intel Pentium(Merom-1M) T3400 2.16GHz))
LBL stands for Line-By-Line(GRAMMATICAL ENGLISH LINES) i.e. sentences not merely CRLF or LF lines!
.LBL files are made from .TXT files which are made from respective .DOC, .RTF, .LIT, .PDF, .CHM, .HTM[L], .DJV[U] files;
Number and size of *.LBL files: 562,504 files(26GB or 27,991,747,152 bytes);
Lines and words in *.LBL files: 424,754,717 lines(with 4,582,451,898 words of them 9,177,221 distinct);

'Monstrous Dumpers' package, revision 13-

OR

How words can be mixed into sentences!?

With this package(the main part of project "GAMERA") you can make full-text(brute-force) requests into millions of lines(sentences). For example: make a search for **requests???????????into** to see whether that preposition has place near on right side of "requests". This package(a Winrar archive) is intended as shareware and contains six very fast 32bit console text tools: Caterpillar (its rivals Raccoondog, Salah-ed-din and Kazuya), Leprechaun, Yoshi and of course 100++ million sentences(in English language) from various sources.

The package allows easily to create: - a FILELIST(a text file with filenames);

- a WORDLIST(a text file with sorted distinct words);

and as a main feature a text-pattern to be searched into LF(Unix)|CRLF(Windows) lines(or files) via filelist and to dump resultant hits(lines or filenames) into .HTML file.







	1							
<u>Main features:</u>								
 26GB english-ASCII-texts converted to .LBL(same as .TXT but each line is a second se	tterns: very erful; zlib used; e to QuickLZ used; <i>iles</i> finally; ntences) compared to							
Installation(i.e. extracting) notes:								
 Unrar in D:\ if possible, "Caterpillar.lnk", "Go to PROMPT.lnk", "Raccoondog.lnk", "Salah-ed-din.lnk", "Kazuya.lnk" need manual adjustments if not D: 7GB must be free. To use "Caterpillar.lnk" and "Salah-ed-din.lnk" and "Kazuya.lnk" must run (R20.BAT) and (R2G.BAT) and (R2L.BAT) respectively. 								
<u>Current revisions of tools:</u>								
- EXEs(windows): Caterpillar r.14+ Leprechaun r.13++ Yoshi r.06 Salah-ed-din r.14++ Raccoondog r.17++ Kazuya r.17++ r.15 has an ability to search non-compressed files tool - ELFs(Linux): Caterpillar r.14+ Leprechaun r.13++ Yoshi r.06 Salah-ed-din r.14+ Raccoondog r.17++								
Notel: Revisions 14++ are Experimental(but operational, not beta) Karp-Rabin function with Note2: Predecessor of <i>Caterpillar</i> , <i>Salah-ed-din</i> & <i>Raccoondog</i> was Kazuya (with more funct- parts written in 16bit assembler), someday I will resurrect him in 64bit.	my nash, see last page. ionality and critical							
<u>Convert at will:</u>								
Use G2R.BAT for .gz -> .lzma (1000+ minutes needed to convert, grmbl) Use R2G.BAT for .lzma -> .gz (11:05 PM - 12:18 AM i.e 73 minutes needed to conver Use R2L.BAT for .lzma -> .Lasse (06:57 PM - 07:45 PM i.e 48 minutes needed to conver Use R2O.BAT for .lzma -> .Okumura (11:33 PM - 01:22 AM i.e 109 minutes needed to conver	rt)							
Some experience(Machine: Toshiba Satellite L305 - Intel Pentium Dual CPU	<u>J T3400 @ 2.16GHz):</u>							
 Caterpillar uses LZSS(based on LZSS.C written by H.Okumura) compression; 24.9GB -> 11.4 GB (12,341,932,922 bytes); delivering text at 82KB(<i>149KB when in system cache</i>)/clock suitable for FAST HDDs 80+MB/s. Raccoondog uses LZMA(based on LZMA SDK 4.65 written by I.Pavlov) compression 	i.e. 80MB/s; on:							
<pre>24.9GB -> 5.5 GB (5,944,631,607 bytes); delivering text at 40KB(bottleneck is CPU power alone)/clock suitable for flash cards like CFs, SDs. - Salah-ed-din uses GZ(based on zlib 1.2.3 written by J.Gailly and M.Adler) comp</pre>	i.e. 39MB/s;							

- Saian-ed-din uses GZ(based on zlib 1.2.3 written by J.Gailly and M.Adler) compression; 24.9GB -> 8.4 GB (9,051,049,655 bytes); delivering text at 117KB(120KB when in system cache)/clock i.e. 114MB/s; suitable for FAST CPUs 3+GHz.

- Kazuya uses LZ(based on QuickLZ 1.4.0 written by Lasse Reinhold) Compression; 24.9GB -> 10.6 GB (11,402,975,168 bytes); delivering text at 88KB(262KB(<u>118KB(EN)</u>) when in system cache)/clock i.e. 85MB/s; suitable for FAST SSDs 115+MB/s. <u>Near future dreams: CPU(2x faster) and SSD(2x115Mb/s read) will give 2x255Mb/s.</u>

<u>Search|Seek|Find in order to Explore|Learn|Avoid Different Styles:</u>

["Супруга съм на три деца. С чувекъ сбрахме пари и купихми триустаен партамент. Една вечер звъни вратата. Звънецъ чука. Отварям - НИНДЖА. И без да каже нищо, с карате в бъбреците. Дукат съ усета ми би два шамара с КРАК и един на детето в гръбначнийъ кош! От ударната вълна отльитам на 20-30 метра. Абстрахираха децата. А чувекъ го нема. Ако общината в града не вземе спешни мерки, ще се самуубеся ильи ще изчезна безкрайно."]

/Интервю с ромка излъчено по КАНАЛ 1 за акция на НСБОП по залавяне на опасни рецидивисти в Пазарджишко./

Enjoy! Sanmayce 'Kaze', 2009 Mar 13.

D:_KAZE_G.S._Corpus>yoshi Yoshi(Filelist Creator), revision 06, written by Svalqyatchx, in fact based on SWEEP.C from 'Open Watcom Project', thanks-thanks. Note1: So far, it works for current directory only. Note2: Default method is depth-first traversal; may use pipe 'yoshi|sort' for breadth-first_like traversal results. Note3: Make notice that '*.*'(extensionfull only) is not equal to '*'(all); one disadvantage is an inability to list only extensionless filenames. Note4: Search is case-insensitive as-must. Note5: This revision allows multiple '*', and meaning of masks is: '?' - any character AND NOT EMPTY(default, for OR EMPTY see option -e); '*' - any character(s) or empty. Note6: What is a .LBL(LineByLine) file? it is a bunch of GRAMMATICAL lines not mere LF or CRLF lines; it contains not symbols under 32(except CR and LF) and above 127; it contains not space symbol sequences. Usage: Usage: Yoshi [option(s)] [filename(s)] option(s): inclust: -v i.e. verbose mode; output goes to console; -f i.e. fullpath mode for output; -e i.e. treat '?' as any character OR EMPTY; -t i.e. touch all encountered files; -2 i.e. convert all encountered .TXT files to .LBL files; -o<filename> i.e. output goes to file(in append mode). ename(c): -v -f -t -2 filename(s):
 wildcards '*' and wildcards '?' are allowed i.e. "str*.c??";
 default filename is '*'; DO NOT FORGET TO PUT
 filename(s) WITH WILDCARD(S) INTO QUOTE MARKS! Examples: Yoshi -v -f -oCaterpillar_NON.lst "*.lbl" "*.txt" "*.htm" "*.html" Yoshi -f -oMyEbooks.txt "*wiley*essential*.pdf" "*russian*.*htm" Yoshi: Total size of files: 00,027,750,342,332 bytes. Yoshi: Total files: 000,000,001,088. Yoshi: Total folders: 0,000,000,003. D:_KAZE_G.S._Corpus>"Leprechaun_r13++_32bits.exe" Leprechaun(Fast Greedy Word-Ripper), revision 13++, written by Svalqyatchx. Leprechaun: 'Oh, well, didn't you hear? Bigger is good, but jumbo is dear.' Kaze: Let's see what a 4-way hash + 6,602,752 Binary-Search-Trees can give us, also the performance of a 4-way hash + 6,602,752 B-Trees of order 3. Leprechaum: 'On, well, didn't you hear? Bigger is good, but jumbo is dear.' kaze: Let's see what a 4-way hash + 6,602,752 B-Trees of order 3. 'The Little Monster' short notes: Note1: I wish to thank to R.N. Horspool, Ranjan Sinha, Dmitry Shkarin, Michael Abrash. J. Bentley, R. Sedgewick, Igor Pavlov, Lasse Reinhold for Sharing their knowledge to public. Note2: Run it without parameters to get usage and short notes. Note3: This simple amateurish(more over I am not versed well neither in C nor in mathematics nor in english language, but I am persistent in INDEXING GBS of CC(Wintows) and CCC(INN)) and the sast its source is create a Word.ist for a group of files(given via filelist). Its name comes(according to Heritage Dittionary) from 'low corpus' or 'little body', in fact from amazing movie saga 'Leprechaun 1-2-3-4-5-6' starring by warwick Davis. Note4: Only words up to 31 chars are proceeded - the reason is 'DDT'(the longest word in Heritage Dictionary 37 dedition) or 'dichlorodiphenyltrichloroethane'. Note5: Curson hiding in C - mission impossible for me. Note6: By default(third parameter is 1023) allocated memory is 393ME. Due to 'malloc() limitation under WINOWS, maximum value of third parameter is 51.74 which is 1980MB allocated block. Note9: Revision 12+ has a buffered 'fread() - therefore 1/0 READ-BUEST SPEED is the first(worst) bottleneck, as a result fr.12+ is much-much faster; the second(worse) bottleneck as a result fr.22+ is much-much faster; the second(worse) bottleneck as a result fr.22+ is much-much faster; the second(worse) bottleneck as a result fr.22+ is much-much faster; the second(worse) bottleneck is the linked lists - the b-trees might be the answer; the linked lists - the b-trees for file 'wikipedia-de-html.tar' 42,291,855,360 bytes with 5,750,179,678 words of them 7,75,373 distinct attempts to Find/Put a WORD into a linked list are 6,117,675,470(r.12++) and 5,845,989,790 (r.12+++); also bot 'if sections were moved because they were executed tor highewises bott in performance only for Toshiba Satellite L305 Mobile DualCore Intel Pentium, 2166 MHz (13 x 167) CPU Type: CPU Alias: Merom-1M L1 Code Cache: L1 Data Cache: 32 KB per core 32 KB per core L2 Cache: 1 MB (On-Die, ECC, ASC, Full-Speed)

Bus Type: Bus Width: Real Clock: Effective Clock: Dual DDR2 SDRAM 128-bit Bus Type: Bus Width: 128-bit Real Clock: 333 MHz (DDR) Effective Clock: 666 MHz EVEREST v5,00.1650 Memory Copy: 3725MB/s with timings 5-5-5-13 result is logged to 'Leprechaun.LoG': Bytes per second performance: 2,860,880w/s Input File with a list of TEXTUAL Files: static.wikipedia.org.downloads_2008-06.e.1.St Size of all TEXTUAL Files: 23,674,511,360 word court: 30,974,750,142 of them 12,561,874 distinct Number of Files: 1 Number of Lines: 2088618575 Allocated memory in MB: 1920 words with length 01 occupy 0,033KB of 0,349KB given i.e. 09% utilization words with length 02 occupy 0,033KB of 0,349KB given i.e. 09% utilization words with length 02 occupy 0,035KB of 0,697KB given i.e. 17% utilization words with length 03 occupy 0,035KB of 0,697KB given i.e. 47% utilization words with length 03 occupy 0,035KB of 0,697KB given i.e. 47% utilization words with length 04 occupy 0,151KB of 0,697KB given i.e. 47% utilization words with length 05 occupy 3,296KB of 6,968KB given i.e. 47% utilization words with length 00 occupy 3,714KB of 6,968KB given i.e. 47% utilization words with length 00 occupy 3,205KB of 6,968KB given i.e. 47% utilization words with length 10 occupy 2,235KB of 5,923KB given i.e. 43% utilization words with length 10 occupy 3,235KB of 5,923KB given i.e. 43% utilization words with length 10 occupy 3,235KB of 5,484KB given i.e. 44% utilization words with length 10 occupy 3,235KB of 1,724KB given i.e. 44% utilization words with length 10 occupy 3,248KB of 1,742KB given i.e. 44% utilization words with length 10 occupy 0,617KB of 1,742KB given i.e. 44% utilization words with length 10 occupy 0,617KB of 1,742KB given i.e. 27% utilization words with length 10 occupy 0,617KB of 1,742KB given i.e. 27% utilization words with length 20 occupy 0,617KB of 1,742KB given i.e. 27% utilization words with length 20 occupy 0,617KB of 1,742KB given i.e. 13% utilization words with length 20 occupy 0,0167KB of 0,697KB given i.e. 13% utilization words with length 20 occupy 0,0167KB of 0,697KB given i.e. 13% utilization words 333 MHZ (DDR) 666 MHZ Usage: Leprechaun InFile OutFile [BufferSize] [SortMethod] [TreeMethod] <InFile>: Input file with files for Leprechauning, in WINDOWS console you can create it by 'E:\KAZEHOME>dir *.txt/s/b>Leprechaun.lst' <OutFile>: Output WORDLIST(sorted since r.9, CRLF) file <BufferSize>: Optional Dynamic RAM buffer in KB, default(and minimum in the same time) is 1023, i.e. omit or specify greater one <SortMethod>: Optional Sort Method, default is 'D', A - InsertionSort B - InsertionX26Sort C - MultiKeyQuickSortSort by J. Bentley, R. Sedgewick D - MultiKeyQuickSortX26Sort' by J. Bentley, R. Sedgewick <TreeMethod>: Optional Tree Method, default is 'X', X - Binary-Search-Trees Y - B-Trees of order 3 Have a nice Leprechauning. For contacts: sanmayce@hotmail.com Sanmayce Svalqyatchx 'Kaze', 2005 Feb 07(rev.13++: 2010 Apr 12). D:_KAZE_G.S._Corpus>Caterpillar Caterpillar(Sentence_Dumper), revision 14+, written by Svalqyatchx, in fact adapted from Haruhiko Okumura's excellent LZSS.C program. How near are these words_forms to me: Masakari, Massacre, Steel-Coloss, Monster-Truck, Dump-Mining-Truck, Caterpillar 797, Liebherr, Komatsu. They resemble one thing: strong-devoid-of-ambition-power(i.e. a pure work/time). 'Caterpillar' is a simple pattern searcher(from 'Masakari' family to into archived english-text files, designed to achieve up to 90% higher read speed than the HDD READ BURST i.e. 'copy hugefile nul' gaining at same time 50% compression of searched data. familv tools) Its main feature is somewhat hidden nowadays, because of pseudo-transparent decompression used, which leads to doubling(unreachable in fact) uploaded data for search function(written by N. Horspool, thanks a lot) due to LZSS algorithm implemented by H. Okumura(greetings to him). Okumura's variant(HDD2RAM) which is much faster(!!!) and needs less memory than tuned memory-to-memory decompression(RAM2RAM) variant. I am still stunned.

In few words: feeding search function is 100-% faster with very fast CPU-Physical_RAM subsystems, in this way reducing the ugly penalty which comes from reading a HDD. In numbers: me IDE HITACHI 7200rpm ZMB gives up to 60MB/s READ BURST, 'Caterpillar' almost doubles(i.e. 120-MB/s) it in case of 3+++GHz CPU and 533+++MHz RAM.

For Windows 2003, VIA KT600, AMD XP 2500+(1836.12MHZ=11x166.92MHZ), FSB 333.84MHz(2x166.92MHz), 512KB L2 cache, 1 DIMM DDR 512MB 333MHz(2x166MHZ), Caterpillar(in fact LZSS) decompresses 58,000KB per second i.e. boost is negative: 60MB/s=61,440KB/s(READ BURST) is greater than 58,000KB/s. But for two times faster CPU-RAM sub-system(SERVER) than described above OR for two times slower HDD sub-system(LAPTOP) boost will be positive: (1 - READ BURST SPEED / DECOMPRESSION SPEED) * READ BURST SPEED or (1 - (61,440KB/s) / (2 * 58,000KB/s)) * 61,440KB/s = (0.471) * 61,440KB/s.

Since revision 5 'fread()' was changed with 'read()', for speed.

'The Monster-Dump-Truck' short notes: Note1: Thanks a lot to N. Horspool, Dmitry Shkarin, H. Okumura, Igor Pavlov. Note2: Run it without parameters to get usage and short notes.

Note3: Current revision searches only for case-sensitive and unexact matches. Note4: This simple amateurish(more over I am not versed well neither in C nor in mathematics nor in english language, but I am persistent in INDEXING GBs of english TEXTS) tool is written in ANSI C(at least its source is compileable for CL(Windows) and not yet for GCC(Linux) because of 'O_BINARY in open(), gets(), getch(), kbhit()', and its purpose is to create a SentenceList for a group of compressed(with it) text files(LF and CRLF) given via filelist. Its name comes from a heavy-nopride-dumper-truck 'Caterpillar'. Note5: By default allocated memory is 95MB i.e. decoding is HDD2RAM. Note6: Disastrous performance in case 95MB liA7MB not fully ohysical! Its name comes from a heavy-nopride-dumper-truck 'Caterpillar'.
Note5: By default allocated memory is 95MB i.e. decoding is HDD2RAM.
Note6: Disastrous performance in case 95MB|147MB not fully physical!
Note7: For me digital library:
 where files are 54, ENcoded 6,917,425,566, DEcoded 14,419,485,826
 with windows XP, VIA KT600, AMD XP 2500+(1836.12MHz=11x166.92MHZ),
 FSB 333.84MHZ(2x166.92MHZ), S12KB L2 cache, DDR 512MB 333MHZ(2x166MHZ),
 IDE HDD Maxtor 80GB 7200 8MB and
 'D:\temp>caterpillar Caterpillar.lst Caterpillar.lst'
 'D:\temp>caterpillar Caterpillar.lst Caterpillar.MAZRAM.ini'
 result is: 282 seconds or 41000KB/s upload, 11000KB/s boost,
 S2000KB/s boosted upload, 56000KB/s decode.
 'D:\temp>caterpillar Caterpillar.lst CaterpillarHDD2RAM.ini'
 result is: 142 seconds or 99000KB/s boosted upload!!!
Note8: Matches(hits) containing neither '<' nor '>' are written
 to 'Caterpillar.hits.pattern?.html' file.
Note9: Works both on UNIX(LF) and Windows(CRLF) text files.
NoteA: Never forget the importance of defragmented_AND_grouped files located at
 fastest area of disk - first partition is faster than second one, etc.
NoteB: In ANSI, clock is defined as '#define CLOCKS_PER_SEC 1000'.
NoteC: Since Caterpillar 13++:
 - limits(just skip longer ones) lines to 960 chars; OTHERWISE: HUGE TIME
 DELAYS due to recursive function;
 - shows hits to console too; MORE VIID;
NoteE: At last NON-ENCODED regime has two modes: in addition to LINE(i.e.
 hits are lines) there is a FILE(i.e. hits are filenames) mode.
NoteF: For all regimes files Caterpillar.HIT?.lst are created for each
 pattern(1,2,3 and 4) - containing hits filelist i.e. filenames
 containing HITS(either LINEs or FILENAMES).
Below is LINE(default for DECODING ???2RAM regimes) mode pattern description: Below is LINE(default for DECODING ???2RAM regimes) mode pattern description: Pattern(s) note: You may specify(four times) a main-pattern(case insensitive with wildcards '*' i.e. any character(s) or empty and '?' i.e. any character or empty) with three nested-patterns(case sensitive and unexact), all four connected with AND. Due to different line endings(CRLF in Windows; LF in UNIX) you must add a '?' wildcard in place of CR: for example in case of searching for '*.pdf' write '*.pdf?'. Pattern(s) example: Pattern1: *take? *it* Pattern1_NestedPattern1: you Possible hit: ... your reason is so taken by It. D:\Caterpillar>copy con MAKElst.bat @echo off dir Caterpillar_tree*.lbl /s/b>Caterpillar_NON.lst dir Caterpillar_tree*.txt /s/b>>Caterpillar_NON.lst echo. Have a nice Caterpillaring. For contacts: sanmayce@hotmail.com Sanmayce Svalqyatchx 'Kaze', 2009 Jan 29. D:_KAZE_G.S._Corpus>Raccoondog.exe LZMA Utility 4.65 : Igor Pavlov : Public domain : 2009-02-03 Usage: lzma <e|d> inputFile outputFile
 e: encode file d: decode file D:_KAZE_G.S._Corpus>Raccoondog -SA4 Raccoondog.lst Raccoondog(LZMA Sentence_Dumper), revision 17++, written by Svalqyatchx, in fact adapted from Igor Pavlov's excellent LZMA 4.56 SDK. Usage1: Raccoondog [-SA1|-SA2|-SA3|-SA4] filename Decodes all files from a list(filename) -SA1 : Brute_Force Search Algorithm -SA2 : Quick_Boyer_Moore Search Algorithm -SA3 : SMITH_Boyer_Moore Search Algorithm Default is HORSPOOL_Boyer_Moore Search Algorithm Usage2: Raccoondog <e|d> inputFile outputFile e: encode file d: decode file Example1: Raccoondog Raccoondog.lst Example2: Raccoondog e Caterpillar.001.txt.lzma Note1: Benchmark: Note1: Benchmark:

Raccoondog(EN:8KB/clock, DE:39KB/clock) for 24.9GB(5.53GB LZMA) texts. Me machine Toshiba Satellite L305 Mobile DualCore Intel Pentium, 2166 MHz (13 x 167) Motherboard Name: CPU Type: CPU Alias: L1 Code Cache: L1 Data Cache: L2 Cache: Merom-1M 32 KB per core 32 KB per core 32 KB per core 1 MB (On-Die, ECC, ASC, Full-Speed) Dual DDR2 SDRAM 128-bit Bus Type: Bus Width Real Clock: Effective Clock: 333 MHz (DDR) Effective Clock: 666 MHż Note2: Disastrous performance in case 128MB not fully physical! Note3: Matches(hits) are overwritten to Raccoondog.hits.Pattern?.html files. Note4: Works both on UNIX(LF) and Windows(CRLF) text files. Note5: Never forget the importance of defragmented_AND_grouped files located at fastest area of disk - first partition is faster than second one, etc. Note6: In ANSI, clock is defined as '#define CLOCKS_PER_SEC 1000'. Note7: Since Raccoondog 13++: - limits(just skip longer ones) lines to 960 chars; OTHERWISE: HUGE TIME DELAYS due to recursive function; - shows hits to console too; MORE VIVID; Note8: Since Raccoondog 14: 666 MHz Note8: Since Raccondog 14:

 No deletion of input file after compressing/decompressing;
 Note9: During execution hitting a 'Esc' causes termination(i.e. skipping rest).

 NoteA: The two examples below show the need of one additional wildcard in order to match CR for windows texts; end of line is LF(as in UNIX):

 Pattern(s) example: Pattern1: #########

 Pattern(s) example: Pattern1. wstedPattern1: Possible hit: NEW YORK Pattern(s) example: Pattern1: \$\$\$\$\$\$\$@ Pattern1_NestedPattern1: Possible hit: Printing Possible hit: Printing Pattern(s) note: You may specify(four times) a main-pattern(case insensitive with wildcards '*' i.e. any character(s) or empty, also '@' i.e. any character or empty, also '#' i.e. any character and not empty, also '\$' i.e. any NON-ALPHA character and not empty), also '%' i.e. any NON-ALPHA character and not empty) with three nested-patterns(case sensitive and unexact), all four connected with AND. Due to different line endings(CRLF in Windows; LF in UNIX) you must add a '@' wildcard in place of CR: for example in case of searching for '*.pdf' write '*.pdf@'. Pattern(s) example: Pattern1: *%take@%\$\$@ Pattern1_NestedPattern1: Possible hit: ... is taken by Possible hit: ... would take it Note: First % is to avoid e.g. 'ing' suffix Master-pattern note: It is case insensitive with wildcards '*','@','#','\$','%' allowed. The purpose of this pattern is to decide whether a search for next patterns will be executed, it is applied on all lines i.e. the whole file. There must be at least one hit in order to execute search for next patterns. for next patterns. Have a nice Raccoondoging. For contacts: sanmayce@hotmail.com Sanmayce Svalqyatchx 'Kaze', 2010 Jun 06. Allocated memory for DEcoded file in MB: 256 Size of input file with files for Raccoondoging: 9669 Input Master-pattern(hit only 'Enter' to skip): Input Pattern1(hit only 'Enter' to skip): *not anymore* - Input Pattern1_NestedPattern1(hit only 'Enter' to skip): Input Pattern2(hit only 'Enter' to skip): Processing .\Caterpillar.001.RAFT2.txt.lzma ... Doing DECODE from HDD to RAM ... Overall decode performance so far: 000,007KB/clock(EN) or 000,031KB/clock(DE) Doing SEARCH for Pattern1 at once and flushing hit-sentences ... 000,000,001 It used to be just "the living room," but not anymore. 000,000,002 But not anymore. Found 2 case-insensitive and unexact matches(hits), so far. 'Esc' was pressed, so skip the rest files and quit! Total Rough Upload and Decode time: 2,297 clocks Total Rough Search time: 1,719 clocks Total time: 4 seconds Total Lines encountered: 1,150,388 Total Lines encountered: 1,150,388 Total Search(non-mask) function invocations: 0 Total Search(MASK i.e. wildcard) function invocations: 1,149,075 Total MASK i.e. wildcard invocations: 1,149,075 Total MASK i.e. wildcard time: 1,434 clocks Total MASK i.e. wildcard time: 1,434 clocks Total MASK i.e. wildcard performance: 46KB/clock Total BoyerMooreHorspool invocations: 0 Total BoyerMooreHorspool (whole chunks, not lines) hits: 0 Total BoyerMooreHorspool(whole chunks, not lines) time: 0 clocks Total KarpRabinKaze(whole chunks, not lines) hits: 0 Total KarpRabinKaze(whole ch D:_KAZE_G.S._Corpus>Salah-ed-din -SA4 Salah-ed-din.lst Salah-ed-din(Sentence_Dumper), revision 14++, written by Svalqyatchx, in fact adapted from Mark Adler's and Jean-loup Gailly's ZLIB package. Usage1: Salah-ed-din [-SA1|-SA2|-SA3|-SA4] filename Decodes all files from a list(filename) -SA1 : Brute_Force Search Algorithm -SA2 : Quick_Boyer_Moore Search Algorithm -SA3 : SMITH_Boyer_Moore Search Algorithm -SA4 : Karp_Rabin_Kaze Search Algorithm Default is HORSPOOL_Boyer_Moore Search Algorithm Usage2: Salah-ed-din [-d] [-f] [-h] [-r] [-1 to -9] [files...] -d : decompress -d : decompress -f : compress with Z_FILTERED -h : compress with Z_HUFFMAN_ONLY -r : compress with Z_RLE

-1 to -9 : compression level

Example1: Salah-ed-din Salah-ed-din.lst Example2: Salah-ed-din -SA2 Salah-ed-din.lst Example3: Salah-ed-din -f -6 Caterpillar.001.txt Example4: Salah-ed-din -d Caterpillar.001.txt.gz Notel: Benchmark: Raccoondog(EN:39KB/clock, DE:117KB/clock) for 24.9GB(8.42GB GZ) texts. Me machine Toshiba Satellite L305 Mobile DualCore Intel Pentium, 2166 MHz (13 x 167) Merom-1M Motherboard Name: CPU Type: CPU Alias: L1 Code Cache: L1 Data Cache: L2 Cache: Bus Type: Bus Width: Real Clock: 32 KB per core 32 KB per core 32 KB per core 1 MB (On-Die, ECC, ASC, Full-Speed) Dual DDR2 SDRAM 128-bit 333 MHz (DDR) Effective Clock: 666 MHz Note2: Disastrous performance in case 128MB not fully physical! Note3: Matches(hits) are overwritten to Salah-ed-din.hits.Pattern?.html files. Note4: Works both on UNIX(LF) and Windows(CRLF) text files. Note5: Never forget the importance of defragmented_AND_grouped files located at fastest area of disk - first partition is faster than second one, etc. Note6: In ANSI, Clock is defined as '#define CLOCKS_PER_SEC 1000'. Note7: Since Salah-ed-din 13++: - limits(just skip longer ones) lines to 960 chars; OTHERWISE: HUGE TIME DELAYS due to recursive function; - shows hits to console too; MORE VIVID; Note8: Since Salah-ed-din 14: - No deletion of input file after compressing/decompressing; Note9: During execution hitting a 'Esc' causes termination(i.e. skipping rest). Effective Clock: 666 MHz Pattern(s) note: You may specify(four times) a main-pattern(case insensitive with wildcards '*' i.e. any character(s) or empty and '?' i.e. any character or empty) with three nested-patterns(case sensitive and unexact), all four connected with AND. Due to different line endings(CRLF in Windows; LF in UNIX) you must add a '?' wildcard in place of CR: for example in case of searching for '*.pdf' write '*.pdf?'. Pattern(s) example: Pattern1: *take? *it* Pattern1_NestedPattern1: you Possible hit: ... your reason is so taken by It. Possible hit: ... your reason is so taken by It. Have a nice Salah-ed-dining. For contacts: sanmayce@hotmail.com Sanmayce Svalqyatchx 'Kaze', 2009 Feb 22. Allocated memory for DEcoded file in MB: 96 Size of input file with files for Salah-ed-dining: 8680 Pattern(s) note: You may specify(four times) a main-pattern(case insensitive with wildcards '*' i.e. any character(s) or empty and '?' i.e. any character or empty) with three nested-patterns(case sensitive and unexact), all four connected with AND. Due to different line endings(CRLF in Windows; LF in UNIX) you must add a '?' wildcard in place of CR: for example in case of searching for '*.pdf' write '*.pdf?'. Pattern(s) example: Pattern1: *take? *it* Pattern1_NestedPattern1: you Possible hit: ... your reason is so taken by It. Input Pattern1(hit only 'Enter' to skip): *not anymore* - Input Pattern1_NestedPattern1(hit only 'Enter' to skip): Input Pattern2(hit only 'Enter' to skip): Processing .\Caterpillar.001.RAFT3.txt.gz ... Doing DECODE from HDD to RAM ... Salah-ed-din decoded buffer size: 99,614,459 Overall decode performance so far: 000,033KB/clock(EN) or 000,102KB/clock(DE) Doing SEARCH for Pattern1 at once and flushing hit-sentences ... 000,000,001 M: Not anymore. 000,000,003 "Not anymore," Lidia replied. 000,000,005 Not anymore. 000,000,005 Not anymore. 000,000,006 "Not anymore. 000,000,008 Not anymore. 000,000,008 Not anymore. 000,000,008 Not anymore. 000,000,009 "Not anymore. 000,000,010 Not anymore. 000,000,011 Not anymore. Found 11 case-insensitive and unexact matches(hits), so far. 'Esc' was pressed, so skip the rest files and quit! Total Rough Upload and Decode time: 953 clocks Total Rough Search time: 1,907 clocks Total time: 3 seconds Total Search(non-mask) function invocations: 0 Total Search(MASK i.e. wildcard) function invocations: 1,834,650 Total Boyer-Moore-Horspool(whole chunks, not lines) hits: 0 Total Boyer-Moore-Horspool(whole chunks, not lines) hits: 0 Total Boyer-Moore-Horspool(whole chunks, not lines) hits: 0 Total Karp_Rabin_Kaze(whole chunks, not lines) time: 0 clocks Salah-ed-din: Done successfully. D:_KAZE_G.S._COrpus>Kazuya.exe/? Kazuya(LZ Sentence_Dumper), revision 17++, written by Svalqyatchx, in fact adapted from Lasse Reinhold's excellent QuickLZ 1.4.0 library, in fact adapted from Ariya Hidayat's sub-excellent FastLZ 0.1.0 library, in fact adapted from Markus F.X.J. Oberhumer's sub-excellent LZO 2.03 library, in fact adapted from Haruhiko Okumura's sub-excellent LZSS 4/6/1989 library. Usagel: Kazuya [-sal|-sa2|-sa3|-sa4|-SA1|-SA2|-SA3|-SA4 [-sA1]-sA2|-sA3|-sA4|-Sa1|-Sa2|-Sa3|-Sa4|-krknd] filename Decodes all files from a list(filename) -sa1 : QuickLZ Decode + Brute_Force Search Algorithm -sa2 : QuickLZ Decode + Quick_Boyer_Moore Search Algorithm -sa3 : QuickLZ Decode + SMITH_Boyer_Moore Search Algorithm -sA1 : LZO Decode + Brute_Force Search Algorithm -SA2 : LZO Decode + Brute_Force Search Algorithm -SA3 : LZO Decode + SMITH_Boyer_Moore Search Algorithm -SA3 : LZO Decode + SMITH_Boyer_Moore Search Algorithm -SA3 : LZO Decode + SMITH_Boyer_Moore Search Algorithm -SA4 : LZO Decode + Karp_Rabin_Kaze Search Algorithm

-sA1 : FastLZ Decode + Brute_Force Search Algorithm -sA2 : FastLZ Decode + Quick_Boyer_Moore Search Algorithm -sA3 : FastLZ Decode + SMITH_Boyer_Moore Search Algorithm -sA4 : FastLZ Decode + Brute_Force Search Algorithm -sa1 : OkumuraLZ Decode + Brute_Force Search Algorithm -sa2 : OkumuraLZ Decode + Karp_Rabin_Kaze Search Algorithm -sa3 : OkumuraLZ Decode + Karp_Rabin_Kaze Search Algorithm -sa4 : OkumuraLZ Decode + Karp_Rabin_Kaze Search Algorithm -s4 : OkumuraLZ Decode + Karp_Rabin_Kaze Search Algorithm -krknd : Karp_Rabin_Kaze Search Algorithm, but with no additional chunk-searches overhead and without decompression of incoming files i.e. pure text is uploaded Default is QuickLZ Decode + HORSPOOL_Boyer_Moore Search Algorithm Usage2: Kazuya <eld[E]D|A|R|a|r> inputFile outputFile e: encode QuickLZ file id ecode QuickLZ file E: encode LZO file A: encode(archive) FastLZ file R: decode(restore) FastLZ file R: decode(restore) OkumuraLZ file Example1: Kazuya Kazuya.lst Example2: Kazuya -SA2 Kazuya.lst Example2: Kazuya -SA2 Kazuya.lst Example3: Kazuya Caterpillar.001.txt Caterpillar.001.txt.Lasse Notel: Benchmark(HDD read speed is the nasty bottleneck): Kazuya(EN:37KB/clock, DE:88KB/clock) for 24.9GB(10.6GB Lasse) texts. Me machine is: motherboard Name: Toshiba Satellite L305 Me machine is: Motherboard Name: Toshiba Satellite L305 Mobile DualCore Intel Pentium, 2166 MHz (13 x 167) Merom-1M CPU Type: CPU Alias: L1 Code Cache: L1 Data Cache: L2 Cache: Bus Type: Bus Width: Real Clock: Meroll-1M 32 KB per core 32 KB per core 1 MB (On-Die, ECC, ASC, Full-Speed) Dual DDR2 SDRAM 128-bit 333 MHz (DDR) Effective Clock: Disastrous performance in case 256MB not fully physical! 666 MHź Note2: Note2: Uisastrous performance in case 256MB not fully physical! Note3: Matches(hits) are overwritten to Kazuya.hits.Pattern?.html files. Note4: works both on UNIX(LF) and Windows(CRLF) text files. Note5: Never forget the importance of defragmented_AND_grouped files located at fastest area of disk - first partition is faster than second one, etc. Note6: In ANSI, clock is defined as '#define CLOCKS_PER_SEC 1000'. Note7: Since Kazuya 13++: limits(just skin longer ones) lines to 960 chars; OTHERWISE: HUES THE Possible hit: NEW YORK Pattern(s) example: Pattern1: \$\$\$\$\$\$\$@ Pattern1_NestedPattern1: Possible hit: Printing Possible hit: Printing Pattern(s) note: You may specify(four times) a main-pattern(case insensitive with wildcards '*' i.e. any character(s) or empty, also '@' i.e. any character or empty, also '#' i.e. any character and not empty, also '\$' i.e. any ALPHA character and not empty), also '%' i.e. any NON-ALPHA character and not empty) with three nested-patterns(case sensitive and unexact), all four connected with AND. Due to different line endings(CRLF in Windows; LF in UNIX) you must add a '@' wildcard in place of CR: for example in case of searching for '*.pdf' write '*.pdf@'. Pattern(s) example: Pattern1: *%take@%\$\$@ Pattern1_NestedPattern1: Possible hit: ... is taken by Possible hit: ... would take it Note: First % is to avoid e.g. 'ing' suffix Master-pattern note: It is case insensitive with wildcards '*','@','#','\$','%' allowed. The purpose of this pattern is to decide whether a search for next patterns will be executed, it is applied on all lines i.e. the whole file. There must be at least one hit in order to execute search for next patterns. for next patterns. Have a nice Kazuyaing For contacts: sanmayce@sanmayce.com Sanmayce Svalqyatchx 'Kaze', 2010 May 24.

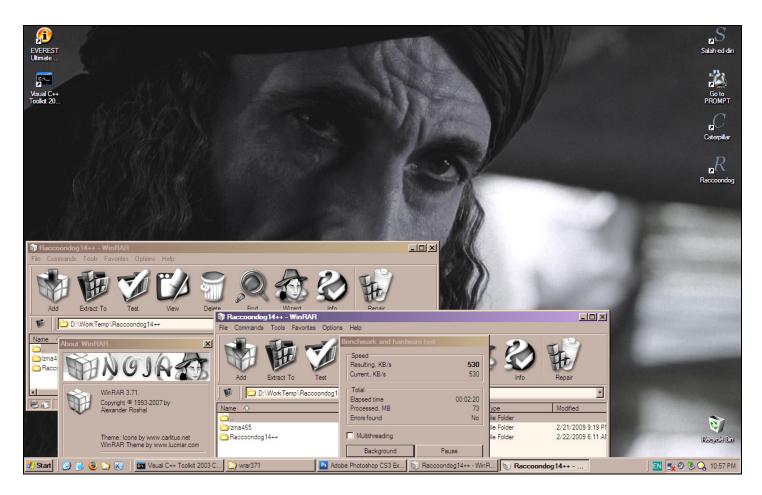
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D:_KAZE_G.S._Corpus>

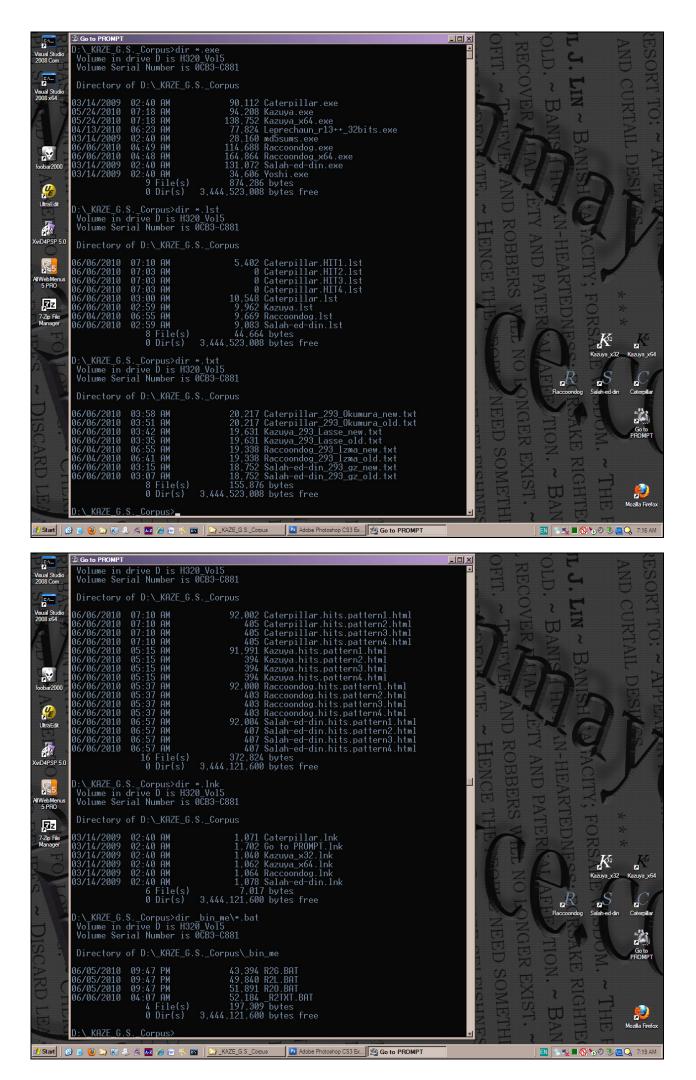
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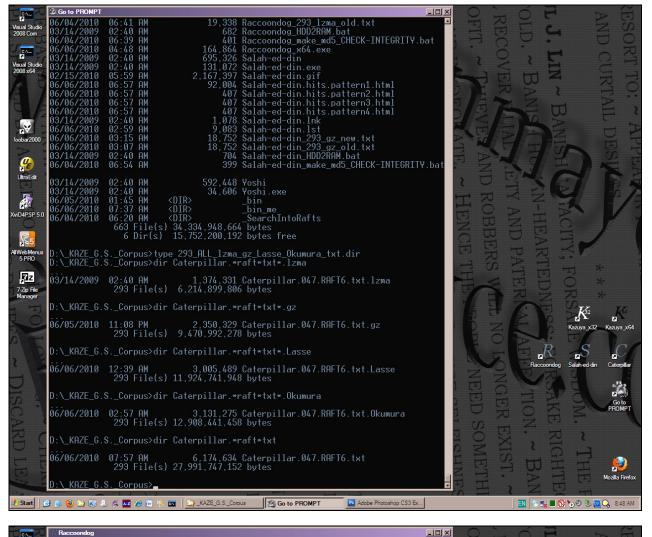
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12/02/2006 10:18 PM	4,385,293 Addison.Wesley.Algorithms.In.Java.3rd.Ed		
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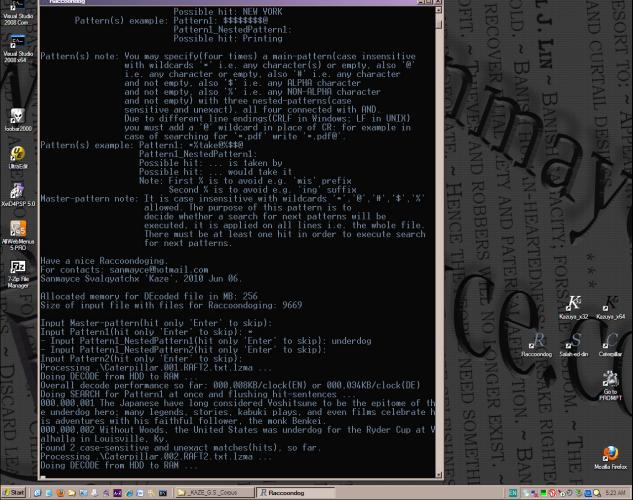


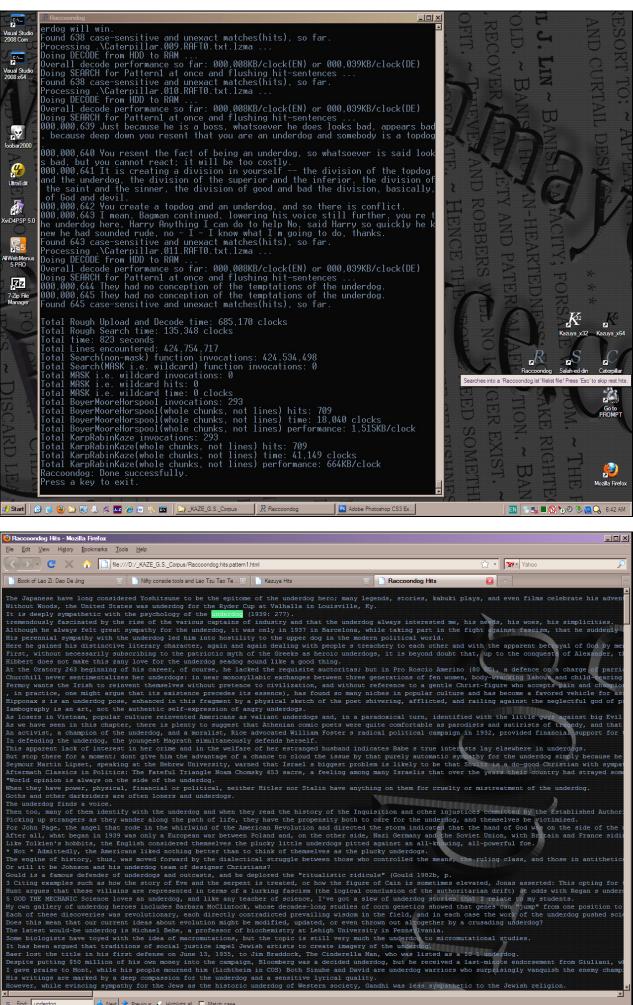




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'Monstrous Dumpers' package, revision 13-, (Downloaded from: www.sanmayce.com): a '_KAZE_G.S._Corpus(LZMA)_2010-Jun-06' short overview; page 16

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ory for pointers-to-words in MB: 36 MultiKeyQuickSortX26Sort' by J. Bentley and R. Sedgewick) ...

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ort pass 26/26 ... lushing sorted words ... ime for sorting unsorted wordlist: 6 second(s) eprechaun: Done.

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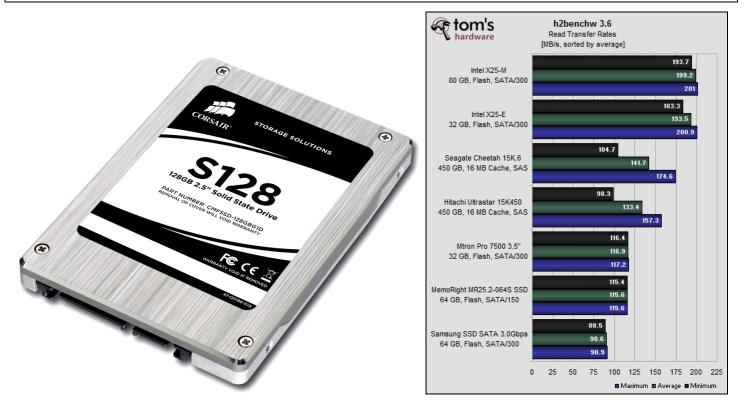
```
D:\_KAZE_G.S._Corpus>type Leprechaun.LOG
Leprechaun report
A(not always THE) Binary-Search-Tree with the longest path(height, PEAK, number of levels):
   ]sysslade]
        []swincian]
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          ]surtaxez[
                  ]suddenne]
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                     [spellchk]
                [spāmmail[
               [shouzoug[
             [shotaike[
           [shitench[
      ]shahrani[
         [sgcenari]
     ]sessionx[
       [sedanais]
  ]schebaum[ ROOT
   ]scaunele]
         [scappard]
      ]scachans[
         [santinha]
     ]sankhaya[
       ]saleeite]
         [saisihan]
Above Binary-Search-Tree with MaxPEAK = 13 has NODEs = 23 and LEAFs = 7
Legend:
At left side of the word - '[' means no left successor
At left side of the word - ']' means left successor exists
At right side of the word - ']' means no right successor
At right side of the word - '[' means right successor exists
Bytes per second performance: 37,522,449B/s
words per second performance: 6,142,696W/s
Input File with a list of TEXTual Files: 293-TXTS_26GB.lst
Size of all TEXTual Files: 27,991,747,152
Word count: 4,582,451,898 of them 9,177,221 distinct
Number Of Files: 293
Number Of Lines: 424754717
Allocated memory in MB: 1950
Number Of Trees(GREATER THE BETTER): 2855919
Forest population(Hash Function Quality regarding Collisions i.e. Hash Table Utilization): <u>43%</u>
Number Of Hash Collisions(Distinct WORDs - Number Of Trees): 6321302
Maximum Attempts to Find/Put a WORD into a Binary-Search-Tree: '13'
Total Attempts to Find/Put WORDs into Binary-Search-Trees: <u>4,746,283,042</u>
Total Number of LEAFs in Binary-Search-Trees(GREATER THE BETTER): 4,361,992
Perfectly-Balanced-Binary-Search-Tree for MaxNODEs = 34 must have PEAK = 6 = rounding down of integer (1+1b(34))
Binary-Search-Tree(1st out of 1) with MaxNODEs = 34 has PEAK = 11 and LEAFs = 11 Binary-Search-Tree(1st out of 2) with MaxPEAK = '13' has NODEs = 23 and LEAFs = 7
Binary-Search-Tree(1st out of 3) with MaxLEAFs = 12 has NODEs = 27 and PEAK = 8
Words with length 01 occupy 0,033KB of 0,162KB given i.e. 19% utilization
Words with length 02 occupy 0,033KB of 0,162KB given i.e. 19% utilization
Words with length 03 occupy 0,040KB of 0,162KB given i.e. 24% utilization
Words with length 04 occupy 0,158KB of 0,646KB given i.e. 24% utilization
Words with length 05 occupy 0,487KB of 1,775KB given i.e. 27% utilization
Words with length 06 occupy 0,991KB of 3,549KB given i.e. 27% utilization
Words with length 07 occupy 1,431KB of 5,968KB given i.e. 23% utilization
Words with length 08 occupy 1,803KB of 7,581KB given i.e. 23% utilization
Words with length 09 occupy 1,643KB of 8,549KB given
                                                                                    i.e. 19% utilization
Words with length 10 occupy 1,546KB of 8,065KB given i.e. 19% utilization
Words with length 11 occupy 1,317KB of 7,420KB given i.e. 17% utilization
Words with length 12 occupy 1,131KB of 6,130KB given i.e. 18% utilization
Words with length 13 occupy 0,945KB of
                                                              5,162KB given i.e. 18% utilization
Words with length 14 occupy 0,796KB of 4,033KB given
                                                                                    i.e. 19% utilization
Words with length 15 occupy 0,662KB of 3,226KB given i.e. 20% utilization
Words with length 16 occupy 0,561KB of 2,904KB given i.e. 19% utilization
Words with length 17 occupy 0,461KB of 2,259KB given i.e. 20% utilization
Words with length 18 occupy 0,394KB of
                                                              1,613KB given
                                                                                    i.e. 24% utilization
Words with length 19 occupy 0,335KB of 1,015KB given i.e. 24% utilization
Words with length 19 occupy 0,335KB of 1,291KB given i.e. 25% utilization
Words with length 20 occupy 0,297KB of 1,130KB given i.e. 26% utilization
Words with length 21 occupy 0,266KB of 0,968KB given i.e. 27% utilization
Words with length 22 occupy 0,220KB of 0,30KB given i.e. 27% utilization
Words with length 22 occupy 0,248KB of 0,807KB given i.e. 30% utilization
Words with length 23 occupy 0,222KB of 0,646KB given i.e. 34% utilization
Words with length 24 occupy 0,210KB of 0,484KB given i.e. 43% utilization
                 length 25 occupy 0,194KB of 0,484KB given i.e. 40% utilization
length 26 occupy 0,178KB of 0,323KB given i.e. 55% utilization
Words with
Words with
Words with length 27 occupy 0,164KB of 0,323KB given i.e. 50% utilization
Words with length 28 occupy 0,160KB of 0,323KB given i.e. 49% utilization
Words with length 29 occupy 0,150KB of 0,323KB given i.e. 46% utilization
Words with length 30 occupy 0,138KB of 0,162KB given i.e. 85% utilization
Words with length 31 occupy 0,134KB of 0,162KB given i.e. 82% utilization
Total pseudo(including hash table) memory utilization: 22%
Total real(wordlist's words VS allocated block) memory utilization: 47/1000
Used value for third parameter in KB: 5000
Use next time as third parameter: 4279-
Time for making unsorted wordlist: 746 second(s)
Time for sorting unsorted wordlist: 6 second(s)
```

```
// 25/7/ = /4,051,159,331,321,99
// 257/8 = 19,031,147,999,601,100,801
// 127/9 = 8,594,754,748,609,397,887
// 57/10 = 362,033,331,456,891,249
// 13/16 = 665,416,609,183,179,841
 // 5^1
                                                                                     244,140,625
815,730,721
             5^12 =
ł
            unsigned int i;
char * pbTargetMax = pbTarget + cbTarget;
char * pbPatternMax = pbPattern + cbPattern;
unsigned long ulBaseTOPOwerMod = 1;
register unsigned long ulHashTarget = 0;
unsigned long ulHashTarget = 0;
bitc = 0:
 long hits = 0;
//unsigned long count;
//char * buf1;
//char * buf2;
              if (cbPattern > cbTarget)
                           return(0);
              // Compute the power of the left most character in base ulBase
//for (i = 1; i < cbPattern; i++) ulBaseToPowerMod = (ulBase * ulBaseToPowerMod);</pre>
                       Calculate the hash function for the src (and the first dst)
              while (pbPattern < pbPatternMax)
                           // Below lines give 366KB/clock for 'underdog':
//ulHashPattern = (ulHashPattern*ulBase + *pbPattern);
//ulHashTarget = (ulHashTarget*ulBase + *pbTarget);
pbPattern++;
//browset;
                           pbTarget++;
                         // Below lines give 436KB/clock for 'underdog' + requirement pattern to be 4 chars min.:
//ulHashPattern = ( (*(long *)(pbPattern-cbPattern)) & 0xffffff00 ) + *(pbPattern-1);
//ulHashTarget = ( (*(long *)(pbTarget-cbPattern)) & 0xffffff00 ) + *(pbTarget-1);
// Below lines give 482KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashPattern = ( (*(unsigned short *)(pbPattern-cbPattern)) | *(pbTarget-1) );
// Below lines give 482KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashTarget = ( (*(unsigned short *)(pbPattern-cbPattern)) | *(pbTarget-1) );
// Below lines give 482KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashPattern = ( (*(unsigned short *)(pbPattern-cbPattern)) & 0xff00 ) + *(pbPattern-1);
//ulHashTarget = ( (*(unsigned short *)(pbPattern-cbPattern)) & 0xff00 ) + *(pbPattern-1);
//ulHashPattern = ( (*(unsigned short *)(pbPattern-cbPattern)) & 0xff00 ) + *(pbPattern-1);
//ulHashTarget = ( (*(unsigned short *)(pbPattern-cbPattern)) & 0xff00 ) + *(pbPattern-1);
//ulHashTarget = ( (*(unsigned short *)(pbPattern-cbPattern)) & 0xff00 ) + *(pbTarget-1);
//ulHashTarget = ( (*(char *)(pbPattern-cbPattern)) <<8 ) + *(pbTarget-1);
// Below lines give 668KB/clock for 'underdog':
ulHashTatren = ( (*(char *)(pbPattern-cbPattern)) <<8 ) + *(pbTarget-1);
// Below lines give 666KB/clock for 'underdog':
ulHashTarget = ( (*(char *)(pbTarget-cbPattern)) <<8 ) + *(pbTarget-1);
// Below lines give 666KB/clock for 'underdog':
ulHashTarget = ( (*(char *)(pbTarget-cbPattern)) <<8 ) + *(pbTarget-1);
// Below lines give 666KB/clock for 'underdog':
ulHashTarget = ( (*(char *)(pbTarget-cbPattern)) <<8 ) + *(pbTarget-1);
// Below lines for the total 
              }
              // Dynamically produce hash values for the string as we go for ( ;; )
                           if
                               f ( (ulHashPattern == ulHashTarget) && !memcmpKAZE(pbPattern-cbPattern, pbTarget-cbPattern, (unsigned int)cbPattern) )
if ( ulHashPattern == ulHashTarget ) {
                                    count = cbPattern;
                                   count = cbPattern;
buf1 = pbPattern-cbPattern;
buf2 = pbTarget-cbPattern;
while ( --count && *(char *)buf1 == *(char *)buf2 ) {
            buf1 = (char *)buf1 + 1;
            buf2 = (char *)buf2 + 1;
            l
                       ||
||
||
                                   }
                                    if ( *((unsigned char *)buf1) - *((unsigned char *)buf2) == 0) hits++;
                                        hits++:
                                        //return((long)(pbTarget-cbPattern));
                          if (pbTarget == pbTargetMax)
    return(hits);
            // Below line gives 482KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashTarget = ( (*(unsigned short *)(pbTarget+1-cbPattern)) | *pbTarget );
// Below line gives 436KB/clock for 'underdog' + requirement pattern to be 4 chars min.:
//ulHashTarget = ( (*(long *)(pbTarget+1-cbPattern)) & 0xffffff00 ) + *pbTarget;
Line 696
//;
//
//
//
                                                          esi, BYTE PTR [ebx]
ecx, DWORD PTR [edx+1]
ecx, -256
ecx, esi
                                  movsx
                                 mov
                                  and
add
                                                                                                                                                                                                ; ffffff00H
             // Below line gives 482KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashTarget = ( (*(unsigned short *)(pbTarget+1-cbPattern)) & 0xff00 ) + *pbTarget;
Line 691
||
||
||
||
                                                           esi, BYTE PTR [ebx]
                                 movsx
                                                           ecx, ecx
cx, WORD PTR [edx+1]
ecx, 65280
                                   xor
                                 mov
                                  and
                                                                                                                                                                                                ; 0000ff00H
                          add ecx, esi
add ecx, esi
// Below line gives 605KB/clock for 'underdog' + requirement pattern to be 2 chars min.:
//ulHashTarget = ( (*(unsigned short *)(pbTarget+1-cbPattern))<<8 ) + *pbTarget;
// Below line gives 668KB/clock for 'underdog':
ulHashTarget = ( (*(char *)(pbTarget+1-cbPattern))<<8 ) + *pbTarget;
718
                                                          ecx, BYTE PTR [eax+1]
edx, BYTE PTR [ebp]
ecx, 8
             Line 718
                                 movsx
                                 movsx
sh1
                          dd ecx, o
   add ecx, edx
// Below line gives 366KB/clock for 'underdog':
//ulHashTarget = (ulHashTarget - *(pbTarget-cbPattern)*ulBaseToPowerMod)*ulBase + *pbTarget;
pbTarget++;
             }
}
```

Core i7 chip(731 million transistors) - the current dominator; plus heart-touching-data-storages:

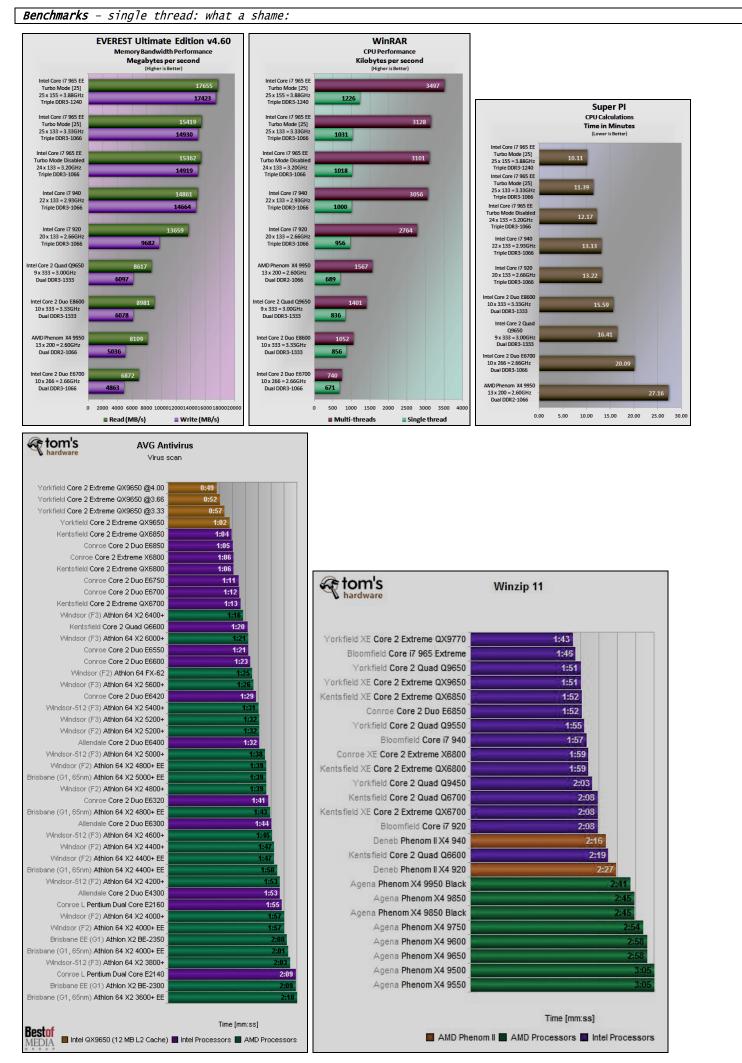


```
SSD drives - simply the future:
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'Monstrous Dumpers' package, revision 13-, (Downloaded from: www.sanmayce.com): a '_KAZE_G.S._Corpus(LZMA)_2010-Jun-06' short overview; page 21

CPUs - gone with the wind like an old newspaper:

for hard	n's _{ware}				Dual Socket	F (1207 F	0 memor	v interface : 2x	dual channel DD	R2-800					07	X .	m's dwar
CPU-Name Athlon 64 FX-74 (4		: 3000 MHz 19	ix free 200		Cache L1/L2 4x 64+64KB / 1MB	¥0 1.35	Itage - 1.40∨	MMX 3DNow! N	Instruction sets SSE SSE2 SSE3 ×86-6	4 PA	Energy Feature Cool'n'Quiet	250 W	Temp. 56°C	Transistors 454 Mio.	Windsor FX	F3	
Athlon 64 FX-72 (4 Athlon 64 FX-70 (4					4x 64+ 64KB / 1MB 4x 64+ 64KB / 1MB Socket AM2 / AM	1.35	- 1.40∨ - 1.40∨ ns) memo	MMX 3DNow! NX	SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6 Iual channel DDR	4 PA	Cool'n'Quiet Cool'n'Quiet 6	250 W 250 W	63°C 63°C	454 Mio. 454 Mio.	Windsor FX Windsor FX		
CPU-Name Phenom X4 9950 BI Phenom X4 9850 BI		2600 MHz 13	xopen 200		Cache L1/L2 4x 64+64KB / 4x 512 KB / 2 ME 4x 64+64KB / 4x 512 KB / 2 ME	1.05	Itage - 1.30V - 1.3V	MMX 3DNow! NX	Instruction sets SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44		Energy Feature Cool'n'Quiet 2 Cool'n'Quiet 2	140 W	Temp. 64°C 61°C	Transistors 463 Mio. 463 Mio.	Agena Agena	B3 B3 B3	
Phenom X4 9850 Phenom X4 9750	4× 4×	2500 MHz 2400 MHz	12.5× 200 12× 200	MHz / HT2000 MHz / HT2000	4x 64+64KB / 4x 512 KB / 2 MB 4x 64+64KB / 4x 512 KB / 2 MB	1.05 1.20/1	- 1.3V .25/1.30V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2	125 W 125 W	61°C 61°C	463 Mio. 463 Mio.	Agena Agena	B3 B3	3 65 n 3 65 n
henom X4 9750 henom X4 9650 henom X4 9550	4×	2300 MHz	11.5× 200	MHz / HT1800	4x 64+ 64KB / 4x 512 KB / 2 ME 4x 64+ 64KB / 4x 512 KB / 2 ME 4x 64+ 64KB / 4x 512 KB / 2 ME	1.10/1.15	/1.20/1.25V /1.20/1.25V /1.20/1.25V	MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2 Cool'n'Quiet 2	95 W	70 °C 70 °C 70 °C	463 Mio. 463 Mio. 463 Mio.	Agena Agena Agena	B3 B3 B3	3 65 n
henom X4 9350e henom X4 9150e	4× 4×	2000 MHz 1800 MHz	10× 200 9× 200	MHz / HT1800 MHz / HT1800	4x 64+64KB / 4x 512 KB / 2 MB 4x 64+64KB / 4x 512 KB / 2 MB	1.0 1.0	- 1.125V - 1.125V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2	65 W 65 W	70 °C 70 °C	463 Mio. 463 Mio.	Agena Agena	B3 B3	3 65 n 3 65 n
<u>henom X4 9100e</u> henom X4 9700 Sa henom X4 9600 BI	imple 4x		12× 200	MHz / HT2000	4x 64+ 64KB / 4x 512 KB / 2 ME 4x 64+ 64KB / 4x 512 KB / 2 ME 4x 64+ 64KB / 4x 512 KB / 2 ME	3 uni	125/1.15V nown /1.20/1.25V	MMX 3DNow! N>	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	XX86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2 Cool'n'Quiet 2	95 W	61°C unknown 70 °C	463 Mio. 463 Mio. 463 Mio.	Agena Agena Agena	B2 B2 B2	2 65 n
henom X4 9600 henom X4 9500	4× 4×	2300 MHz 2200 MHz	11.5x 200 11x 200	MHz / HT1800 MHz / HT1800	4x 64+64KB / 4x 512 KB / 2 MB 4x 64+64KB / 4x 512 KB / 2 MB	3 1.10/1.19 3 1.10/1.19	/1.20/1.25V /1.20/1.25V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2	95 VV 95 VV	70 °C 70 °C	463 Mio. 463 Mio.	Agena Agena	B2 B2	2 65 m 2 65 m
henom X3 8750 henom X3 8650 henom X3 8600	3×	2300 MHz	11.5× 200	MHz / HT1800	3x 64+ 64KB / 3x 512 KB / 2 ME 3x 64+ 64KB / 3x 512 KB / 2 ME 3x 64+ 64KB / 3x 512 KB / 2 ME	1.05	/ - 1.25V / - 1.25V / - 1.25V	MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2 Cool'n'Quiet 2	95 W	70 °C 70 °C 70 °C		Toliman Toliman Toliman	B3 B3 B2	3 65 r
henom X3 8450 henom X3 8400	3x 3x	2100 MHz 2100 MHz	10.5× 200 10.5× 200	MHz / HT1800	3x 64+64KB / 3x 512 KB / 2 MB 3x 64+64KB / 3x 512 KB / 2 MB	1.05 1.05	√- 1.25V √- 1.25V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 SSE44	x86-64 PA x86-64 PA	Cool'n'Quiet 2 Cool'n'Quiet 2	95 W 95 W	70 °C 70 °C		Toliman Toliman	B3 B2	3 65 r 2 65 r
thion 64 X2 6500+ thion 64 FX FX-62 thion 64 X2 6400+ I	2x	2800 MHz 14			2x 64+ 64KB / 2x 512 KB / 2 ME 2x 64+ 64KB / 1MB 2x 64+ 64KB / 1MB	1.35	- 1.40V	MMX 3DNow! NX	SSE SSE2 SSE3 SSE44 SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6	4 PA	Cool'n'Quiet 2 Cool'n'Quiet Cool'n'Quiet	95 W 125 W 125 W	55-63°C	227 Mio. 227 Mio.	Kuma Windsor Windsor	B3 F2 F3	2 90 1
thion 64 X2 6000+ thion 64 X2 5600+	2x 2x	3000 MHz 2800 MHz	15× 200 14× 200	MHz / HT1000 MHz / HT1000	2x 64+64KB / 1MB 2x 64+64KB / 1MB	1.35 1.30	- 1.40∨ - 1.35∨	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 ×86-6 SSE SSE2 SSE3 ×86-6	4 PA 4 PA	Cool'n'Quiet Cool'n'Quiet	125 W 89 W	55-63°C 55-70°C	227 Mio. 227 Mio.	Windsor Windsor	E F3	1 0 0 8 1 0 0 8
thion 64 X2 5400+ I thion 64 X2 5400+ thion 64 X2 5200+	2×		14× 200	MHz / HT1000	2x 64+ 64KB / 512 KB 2x 64+ 64KB / 512 KB 2x 64+ 64KB / 1MB	1.30	- 1.35V - 1.35V //1.35V	MMX 3DNow! N	SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6	4 PA	Cool'n'Quiet Cool'n'Quiet Cool'n'Quiet	89 W 89 W 89 W	55-70°C 55-70°C 55-70°C	154 Mio. 154 Mio. 227 Mio.	Windsor-51 Windsor-51 Windsor	2 F3 2 F3 F3	3 90 1
thion 64 X2 5000+ I thion 64 X2 5000+	Black 2x 2x	2600 MHz 2600 MHz	13× 200 13× 200	MHz / HT1000 MHz / HT1000	2x 64+64KB / 512 KB 2x 64+64KB / 512 KB	1.30\	//1.35V //1.35V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 ×86-6 SSE SSE2 SSE3 ×86-6	4 PA 4 PA	Cool'n'Quiet Cool'n'Quiet	89 W 89 W	55-70°C 55-70°C	154 Mio. 154 Mio.	Windsor-51 Windsor-51	.2 F2 .2 F2	1 0 0 2 3 1 0 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1 1
thion 64 X2 5000+ thion 64 X2 4800+ thion 64 X2 4600+	2x	2400 MHz	12× 200	MHz / HT1000	2x 64+ 64KB / 512 KB 2x 64+ 64KB / 1MB 2x 64+ 64KB / 512 KB	1.30\	(/1.35V //1.35V //1.35V	MMX 3DNow! NX	SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6 SSE SSE2 SSE3 x86-6	4 PA	Cool'n'Quiet Cool'n'Quiet Cool'n'Quiet	89 W 89 W 89 W	55-70°C 55-70°C 55-70°C	154 Mio. 227 Mio. 154 Mio.	Windsor-51 Windsor Windsor-51	- F2	2 90 1
thion 64 X2 4400+ thion 64 X2 4200+	2x 2x	2200 MHz 2200 MHz	11x 200 11x 200	MHz / HT1000 MHz / HT1000	2x 64+64KB / 1MB 2x 64+64KB / 512 KB	1.30\	//1.35V //1.35V	MMX 3DNow! NX MMX 3DNow! NX	SSE SSE2 SSE3 ×86-6 SSE SSE2 SSE3 ×86-6	4 PA 4 PA	Cool'n'Quiet Cool'n'Quiet	89 W 89 W	55-70°C 55-72°C	227 Mio. 154 Mio.	Windsor Windsor-51	2 F2	2 90 i 2 90 i
thion 64 X2 4000+ thion 64 X2 3800+					2x 64+64KB / 1MB 2x 64+64KB / 512 KB		//1.35V //1.35V		SSE SSE2 SSE3 ×86-6		Cool'n'Quiet Cool'n'Quiet	89 W 89 W	55-70°C 55-70°C	227 Mio. 154 Mio.	Windsor Windsor-51	.2 F2	
se to	m'	s														tor	m
A hard	dwar	e			Socket 771 memo	ory interf	ace : dual	/ quad chan	el FB-DIMM DI	DR3-667/8	00				- K	hard	lwar
CPU-Name ore 2 Extreme		Clock 4x 3200 MHz	Multi 8x (open)	FSB 400 MHz QDI	Cache L1/L2 R 4x 32+32 / 2x 6144 KB				SSE4.1 NX EM64T V	T TM2 C18			 Trans. 820 Mi 		field XE C	Stepping	Proce
CPU-Name		Clock	Multi	FSB	Socket 775 memory Cache L1/L2	Voltage		Instruction	n sets	Energy	Features Pow				ore	Stepping	Proce
ore 2 Extreme ore 2 Extreme	QX9770 QX9650 Q9650		9x (open)	333 MHz QDI	R 4x 32+32 / 2x 6144 KB R 4x 32+32 / 2x 6144 KB R 4x 32+32 / 2x 6144 KB	1.3625 V 1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 130	W 64.5°		o. York	field XE 🛛 🔾	1 10 C1	45 45 45
ore 2 Quad ore 2 Quad ore 2 Quad	Q9550 Q9550 Q9450	4x 2833 MHz 4x 2666 MHz	8.5×	333 MHz QDI	R 4x 32+32 / 2x 6144 KB R 4x 32+32 / 2x 6144 KB R 4x 32+32 / 2x 6144 KB	1.3625 V 1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 95	W 71.4°	C 820 Mi	o. Yoi	kfield C		45
ore 2 Quad ore 2 Quad ore 2 Quad	Q9400 Q9300	4x 2666 MHz 4x 2500 MHz	8x	333 MHz QDI	R 4x 32+32 / 2x 3072 KB R 4x 32+32 / 2x 3072 KB	1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 95	w 📃		Yor	kfield C	1	45
ore 2 Quad ore 2 Duo	Q8200 E8600	4x 2333 MHz 2x 3333 MHz		333 MHz QDI	R 4x 32+32/2x 2048 KB R 2x 32+32/1x 6144 KB		MMX SSE S MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18 T TM2 C18	EIST 95 EIST 65	W	410 Mid	Yoi b. Wo		41	45
ore 2 Duo ore 2 Duo	E8500 E8400	2x 3166 MHz 2x 3000 MHz	9.5× 9×	333 MHz QDI	R 2x 32+32 / 1x 6144 KB R 2x 32+32 / 1x 6144 KB	1.3625 V 1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 65	W 72.4°	C 410 Mid	o. We	lfdale C	.0 .0	45 45
ore 2 Duo ore 2 Duo	E8300 E8200	2x 2833 MHz 2x 2666 MHz	8.5× 8×	333 MHz QDI	R 2x 32+32 / 1x 6144 KB R 2x 32+32 / 1x 6144 KB	1.3625 V 1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 65	W 72.4°	C 410 Mid	o. Wo	lfdale C	10 10 10	45
ore 2 Duo ore 2 Duo ore 2 Duo	E8190 E7300 E7200	2x 2666 MHz 2x 2666 MHz 2x 2533 MHz		266 MHz QDI	R 2x 32+32 / 1x 6144 KB R 2x 32+32 / 1x 3072 KB R 2x 32+32 / 1x 3072 KB	1.3625 V	MMX SSE S	SE2 SSE3 SSSE3	SSE4.1 NX EM64T V SSE4.1 NX EM64T V SSE4.1 NX EM64T V	T TM2 C18	EIST 65	W		Wo	lfdale N	.0 40 40	45 45 45
ore 2 Extreme ore 2 Extreme	QX6850 QX6800	4x 3000 MHz	9x (open)	333 MHz QDI	R 4x 32+32 / 2x 4096 KB R 4x 32+32 / 2x 4096 KB	1.37 V	MMX SSE S	SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18	EIST 130	W 64.5°	C 582 Mi	io. Kent	sfield XE	50 13 G0	65
ore 2 Extreme ore 2 Quad	QX6700 Q6700	4x 2666 MHz 4x 2666 MHz	10x (open)	266 MHz QDI	R 4x 32+32 / 2x 4096 KB R 4x 32+32 / 2x 4096 KB	1.37 V 1.37 V	MMX SSE S MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T VT NX EM64T VT	TM2 C18 TM2 C18	EIST 130	W 65°C	: 582 Mi	o. Kent	field XE B	13 50	65
ore 2 Quad ore 2 Quad	Q6600 Q6600	4x 2400 MHz 4x 2400 MHz	9x	266 MHz QDI	R 4x 32+32 / 2x 4096 KB R 4x 32+32 / 2x 4096 KB	1.37 V 1.37 V	MMX SSE S	SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18 TM2 C18	EIST 95	W 71°0	582 Mi	o. Ken	tsfield G	13 50	65 65
ore 2 Extreme ore 2 Duo	X6800 E6850	2x 2933 MHz 2x 3000 MHz	9×	333 MHz QDI	R 2x 32+32 / 1x 4096 KB R 4x 32+32 / 2x 4096 KB	1.35 V	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18	EIST 65	W 72°C	291 Mi	o. Co	onroe G	0 B1 B2	65
ore 2 Duo ore 2 Duo ore 2 Duo	E6750 E6550 E6540	2x 2666 MHz 2x 2333 MHz 2x 2333 MHz	7x 5	333 MHz QDI	R 4x 32+32 / 2x 4096 KB R 4x 32+32 / 2x 4096 KB R 4x 32+32 / 2x 4096 KB	1.5 V 1.5 V 1.5 V	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3 SSE2 SSE3 SSSE3	NX EM64T VT	TM2 C18 TM2 C18 TM2 C18	EIST 65	W 72°C	: 291 Mi	o. Co	onroe 🤅	50 50 50	65 65 65
ore 2 Duo ore 2 Duo ore 2 Duo	E6700 E6600	2x 2667 MHz 2x 2400 MHz	10x	266 MHz QDI	R 2x 32+32 / 1x4096 KB R 2x 32+32 / 1x4096 KB R 2x 32+32 / 1x4096 KB	1.35 V 1.32 V	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18 TM2 C18	EIST 65	₩ 60.1°	C 291 Mi	o. Co	onroe B	0 B1 B2 0 B1 B2	65
ore 2 Duo ore 2 Duo	E6400 E6300	2x 2133 MHz 2x 1866 MHz	7x	266 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x2048 KB	1.35 V 1.35 V	MMX SSE S MMX SSE S	SE2 SSE3 SSSE3	NX EM64T VT NX EM64T VT	TM2 C18 TM2 C18	EIST 65	W 61.4°	C 294 Mi	o. Conr	oe-2048 B	12 12	65 65
ore 2 Duo ore 2 Duo	E6420 E6320	2x 2133 MHz 2x 1866 MHz	7×	266 MHz QDI	R 2x 32+32 / 1x4096 KB R 2x 32+32 / 1x4096 KB R 2x 32+32 / 1x2048 KB	1 2214	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18 TM2 C18	EIST 65	W 60.1°	C 291 Mi	o. Co	onroe B	12	65 65
ore 2 Duo ore 2 Duo ore 2 Duo	E6400 E6300 E4700	2x 2133 MHz 2x 1866 MHz 2x 2600 MHz	7x	266 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x2048 KB	1.32 V 1.32 V 1.312 V	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T VT	TM2 C18 TM2 C18 TM2 C18	EIST 65	W 61.4°	C 167 Mi	o. Alle	endale B	0 B1 B2 L2 1 B2 L2 50	65 65 65
ore 2 Duo	E4600	2x 2400 MHz 2x 2200 MHz	12x	200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x2048 KB	1.312 V	MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3	NX EM64T	TM2 C18 TM2 C18	EIST 65	W 73.3°	C 167 Mi	o. Alle	endale N	40 2 M0	65
	E4500		40.										C 167 Mi		endale L endale L	2	65 65
ore 2 Duo ore 2 Duo ore 2 Duo	E4400 E4300	2× 2000 MHz 2× 1800 MHz	9x	200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x2048 KB	1.31 V 1.32 V	MMX SSE S	SE2 SSE3 SSSE3	NX EM64T	TM2 C18 TM2 C18	EIST 65	W 61.4°	C 167 Mi			40 40	65 65
ore 2 Duo ore 2 Duo ore 2 Duo intium Dual Core intium Dual Core	E4400 E4300 E2220 E2200	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz	9x 12x 11x	200 MHz QDI 200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 2x 32+32 / 1x1024 KB	1.32 V 1.31 V 1.31 V	MMX SSE S MMX SSE S MMX SSE S	SE2 SSE3 SSSE3 SE2 SSE3 SSSE3 SE2 SSE3 SSSE3 SE2 SSE3 SSSE3 SSE2 SSE3 SSSE3	NX EM64T NX EM64T NX EM64T	TM2 C18 TM2 C18 TM2 C18	EIST 65 EIST 65 EIST 65	W 61.4° W W		Co	nroe-L N	40	
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core	E4400 E4300 E2220 E2200 E2180 E2160	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz 2x 2000 MHz 2x 1800 MHz	9x 12x 11x 10x 9x	200 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V	MMX SSE S MMX SSE S MMX SSE S MMX SSE S MMX SSE S	SE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3	NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T	TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 C18	EIST 65	W 61.4° W W W 01.4°	<u>с 167 Мі</u> с	Co Co Co Co	nroe-L M nroe-L M nroe-L L	40 2 G0 M0 2 G0 M0	65
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core entium Dual Core eleron	E4400 E4300 E2220 E2200 E2180 E2160 E2140 E1400 E1200	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz 2x 2000 MHz 2x 1800 MHz 2x 1600 MHz 2x 1600 MHz 2x 1600 MHz	9x 12x 11x 10x 9x 8x 10x 8x	200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 2x 32+32 / 512 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 2 V 1.312 V	MMX SSE S MMX SSE S MMX SSE S MMX SSE S MMX SSE S MMX SSE S MMX SSE S	SE2 SSE3 SSE3 SSE2 SSE3 SSE3 SSE3 SSE3 SSE3	NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T	TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 C18 TM2 TM2 TM2	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 65	W 61.4° W W W 61.4° W 61.4° W 73.3° W 73.3°	C 167 Mi	Co Co Co Co Co	nroe-L N nroe-L N nroe-L L nroe-L L nroe-L N nroe-L N	2 G0 M0 <u>2 G0 M0</u> 40 40	65 65 65
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core entium Dual Core eleron eleron eleron	E4400 E4300 E2220 E2200 E2180 E2160 E2140 E1400 E1200 440 430	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz 2x 2000 MHz 2x 1800 MHz 2x 1600 MHz 2x 1600 MHz 2x 1600 MHz 2000 MHz 1800 MHz	9x 12x 11x 10x 9x 8x 10x 8x 10x 9x 9x	200 MHz QDI 200 MHz QDI	R 2x 32+32/1x2048 KB R 2x 32+32/1x1024 KB R 2x 32+32/512 KB R 32+32/512 KB R 32+32/512 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.312 V 1.312 V 1.375 V 1.3375 V	MMX SSE 5 MMX SSE 5	SE2 SSE3 SSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3	NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T	TM2 C18 TM2 TM2 TM2 TM2 TM2 TM2	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 65 65 65 65 35	W 61.4° W W W 61.4° W 61.4° W 61.4° W 73.3° W 60.4° W 60.4° W 60.4°	C 167 Mi	Co Co Co Co Co Co Co Co	nroe-L N nroe-L L nroe-L L nroe-L N nroe-L N nroe-L A nroe-L A	2 G0 M0 <u>2 G0 M0</u> 40 40 41 41	65 65 65 65 65 65
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core eleron eleron eleron eleron eleron eleron eleron eleron	E4400 E4300 E2220 E2200 E2180 E2160 E2140 E1400 E1200 440 430 420 965	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz 2x 2000 MHz 2x 1800 MHz 2x 1600 MHz 2x 1600 MHz 1800 MHz 1600 MHz 2x 3724 MHz	9x 12x 11x 10x 9x 8x 10x 8x 10x 9x 8x 10x 10x 10x 10x 10x 10x 10x 10	200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 2x 32+32 / 512 KB R 32+32 / 512 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 2 V 1.312 V 1.3175 V 1.3375 V 1.3375 V 1.30 V	MMX SSE 5 MMX SSE 5	SE2 SSE3 SSE3 SSE2 SSE3 SSSE3 SSE2 SSE3 SSSE3 SSE3 SSSE3 SSSE3 SSE3 SSSE3 SSE3 SSS SSE3 SSSE3 SSE3 SSS SSE3 SSE3 SSS SSE3 SSE3 SSS SSE3 SSE3 SSS SS SSE3 SSE3	NX EM64T NX EM64T G4T VT HT	TM2 C18 TM2	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 65 35 35 35 35	W 61.4° W W W 61.4° W 61.4° W 61.4° W 61.4° W 61.4° W 61.4° W 60.4° W 60.4° W 60.4° W 60.4° W 68.6°	C 167 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co	hrroe-L N hrroe-L L hrroe-L L hrroe-L N hrroe-L N hrroe-L A hrroe-L A rester C	2 G0 M0 2 G0 M0 40 40 40 41 41 41 41 41	65 65 65 65 65 65 65 65
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core entium Dual Core eleron eleron eleron eleron eleron entium EE entium EE entium EE	E4400 E4300 E2220 E2200 E2180 E2160 E1400 E1400 E1200 440 430 420 965 955	2× 1800 MHz 2× 2400 MHz 2× 2000 MHz 2× 1800 MHz 2× 1800 MHz 2× 1600 MHz 1600 MHz 1600 MHz 2× 3466 MHz 2× 3466 MHz	9x 12x 11x 10x 9x 8x 10x 9x 8x 10x 9x 8x 10x 14x 13x 18x	200 MHz QDI 200 MHz QDI 266 MHz QDI 266 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 32+32 / 512 KB R 32+32 / 512 KB R 32+32 / 512 KB R 32+32 / 512 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB	1.32 V 1.31 V 1.32 V 1.3375 V 1.30 V 1.30 V 1.30 V 1.30 V	MMX SSE S MMX SSE S	SE2 SE3 SSE3 SSE2 SSE3 NX EM SSE2 SSE3 NX EM	NX EM64T NX EM64T IX EM6	TM2 C18 TM2 TM2 TM2 TM2 TM2 TM2 TM1 TM1 C18	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 65 35 35 35 35 130 130	W 61.4° W W W 61.4° W 61.4° W 61.4° W 73.3° W 73.3° W 60.4°	C 167 Mi C C C C C C C C C 376 Mi C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	hroe-L N hroe-L L hroe-L L hroe-L N hroe-L N hroe-L A hroe-L A hroe-L A rester C rester B	2 G0 M0 2 G0 M0 40 40 41 41 41 41 41 41 41 41 41 41 41 41 41	65 65 65 65 65 65 65 65 65 65
ore 2 Duo ore 2	E4400 E4300 E2220 E2200 E2180 E2180 E1400 E1200 440 430 420 965 955	2× 1800 MHz 2× 2400 MHz 2× 2200 MHz 2× 1800 MHz 2× 1600 MHz 2× 1600 MHz 2× 000 MHz 1800 MHz 1600 MHz 2× 3466 MHz	9x 12x 11x 10x 9x 8x 10x 8x 10x 8x 10x 8x 10x 10x 10x 10x 10x 10x 10x 10	200 MHz QDI 200 MHz QDI 266 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 3x 32+32 / 512 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB R 12kµOps+16/2048 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 2 V 1.312 V 1.3275 V 1.3375 V 1.3375 V 1.30 V	MMX SSE 5 MMX SSE 5 MX SMX SMX SMX SMX SMX SMX SMX SMX SMX S	SE2 SE3 SSE3 SSE2 SE5 SSE5 SSE2 SSE5 SSE5 SSE2 SSE5 SSE5 SSE5 SSE5 SSE5 SSE5 SSE5 SSE5	NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T G4T VT HT 64T VT 64T VT 64T VT	TM2 C18 TM2 C18 TM2 C16 TM2 C16 TM2 C16 TM2 C16 TM2 C16 TM2 C16 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM1	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 65 65 35 35 35 35 35 35 35 35 35 35 35 35 35	₩ 61.4° ₩ ₩ ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 61.4° ₩ 60.4° ₩ 60.4° ₩ 60.4° ₩ 60.6° ₩ 86.6° ₩ 86.6° ₩ 83.6°	C 167 Mi C C C C C C C C C 376 Mi C 376 Mi C 376 Mi C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	hroe-L N hroe-L N hroe-L L hroe-L N hroe-L N hroe-L A hroe-L A hroe-L A hroe-L A rester C ester B rester C rester B rester B rester B	2 G0 M0 2 G0 M0 40 40 41 41 41 51 51	65 65 65 65 65 65 65 65 65 65 65 65
ore 2 Duo ore 2	E4400 E4300 E2220 E2200 E21800 E21800 E1400 E1400 E1400 440 440 440 420 965 955 960 950	2x 1800 MHz 2x 2400 MHz 2x 2200 MHz 2x 2000 MHz 2x 1600 MHz 2x 1600 MHz 2x 000 MHz 1600 MHz 1600 MHz 2x 3724 MHz 2x 3600 MHz 2x 3600 MHz 2x 3600 MHz 2x 3400 MHz 2x 3400 MHz 2x 3200 MHz 2x 3200 MHz	9x 12x 11x 10x 9x 8x 10x 8x 10x 9x 10x 9x 10x 10x 10x 10x 10x 10x 10x 10	200 MHz QDI 200 MHz QDI	R 2x 32+32 / 1x2048 KB R 2x 32+32 / 1x1024 KB R 2x 32+32 / 512 KB R 32+32 / 512 KB R 32+32 / 512 KB R 12kµOp±16/2048 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.32 V 1.375 V 1.375 V 1.375 V 1.375 V 1.30 V 1.30 V 1.30 V 1.30 V 1.30 V 1.31 V 1.32 V 1.	MMX SSE MMX MX MMX MX	SE2 SE3 SE3 SE3 SE2 SE3 SSE3 SE2 SE3 SE4 SE3 SE4 SE3 SE4	NX EM64T NX EM64T G4T VT 64T VT 64T VT 64T VT	TM2 C18 TM2 TM1 TM1 C18	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 35 35 35 35 35 35 35 35 35	W 61.4° W W W 61.4° W 60.4° W 68.6° W 68.6° W 86.6° W 63.4°	C 167 Mi C C C C C C C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroe-L N nroe-L L nroe-L L nroe-L N nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A esler C rester B rester C rester C rester C	2 G0 M0 2 G0 M0 40 40 41 41 41 41 41 41 41 41 41 41	65 65 65 65 65 65 65 65 65 65 65 65 65
ore 2 Duo ore 2	E4400 E4300 E2200 E2200 E2180 E2140 E1400 E1200 440 440 420 965 955 955 950 955 950 955 950 955 940 935	2x1800 MHz 2x2400 MHz 2x2000 MHz 2x2000 MHz 2x1800 MHz 2x1800 MHz 2x1600 MHz 2x1600 MHz 2x3600 MHz 2x3400 MHz 2x3466 MHz 2x3466 MHz 2x3460 MHz 2x3460 MHz 2x3460 MHz 2x3400 MHZ	9x 12x 12x 10x 9x 8x 10x 9x 10x 9x 10x 10x 10x 10x 10x 10x 10x 10	200 MHz QDI 200 MHz QDI	R 2x32+32/1x2048 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/12 KB R 32+32/512 KB R 32+32/512 KB R 32+32/512 KB R 32+32/512 KB R 12kµOps+16/2048 KB	1.32 V 1.31 V 1.30 V 1.31 V 1.32 V 1.32 V 1.30 V 1.30 V 1.30 V 1.32 V	MMX SSE MMX SSE	SE2 2 SE3 SE3 2 SE3 SSE3 SSE3	NX EM64T NX EM64T EM64T NX EM64T NX EM6	TM2 C18 TM2 C16 TM2 C16 TM2 C16 TM2 C18 TM2 TM2 TM1 TM1 C18 TM1 C18 TM1 C18 TM1 C18	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 35 35 130 130 130 95 133 95 95 133	W 61.4° W W W 61.4° W 61.4° W 73.3° W 60.4° W 73.3° W 60.4° W 63.6° W 66.6° W 86.6° W 63.4°	C 167 Mi C C C C C C C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroe-L N nroe-L L nroe-L L nroe-L L nroe-L N nroe-L A nroe-L A nroe-L A resiler C esiler B esiler C esiler B esiler C esiler B esiler C esiler B esiler C esiler B esiler C	2 GO MO 2 GO MO 40 40 41 41 41 41 41 41 41 41 41 41	65 65 65 65 65 65 65 65 65 65 65 65 65 6
ore 2 Duo ore 2 Duo ore 2 Duo re 2 Duo re 2 Duo retuine Dual Core influm Dual Core influm Dual Core influm Dual Core influm Dal Core ieron	E4400 E4300 E2220 E2200 E2180 E2160 E1400 E1200 E1200 440 430 430 430 965 955 960 955 950 950 950 950 950 950 950 950 95	2x1800 MHz 2x2400 MHz 2x2400 MHz 2x200 MHz 2x1800 MHz 2x1800 MHz 2x1600 MHz 2x1600 MHz 2x1600 MHz 1x00 MHz 1x00 MHz 2x3406 MHz 2x3406 MHz 2x3406 MHz 2x3406 MHz 2x3406 MHz 2x3400 MHz 2x3400 MHz 2x3600 MHz 2x3600 MHz 2x3600 MHz 2x3600 MHz 2x3000 MHz 2x300	9x 12x 11x 10x 9x 8x 10x 9x 8x 10x 9x 10x 9x 10x 9x 10x 9x 10x 10x 10x 11x 11x	200 MHz QDI 200 MHz QDI 266 MHz QDI 200 MHz QDI	R 2x32+32/12/048 KB R 2x32+32/12/024 KB R 2x32+32/12/024 KB R 2x32+32/12/024 KB R 2x32+32/12/024 KB R 2x32+32/12/024 KB R 2x32+32/12 KB R 32+32/512 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.375 V 1.375 V 1.375 V 1.375 V 1.375 V 1.30 V 1.31 V 1.30 V 1.31 V	MMX SSE MMX SSE	SE2 SE3 SE3 SSE3 SS	NX EM64T NX EM64T G4T VT G4T	TM2 C18 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM1 C18 TM1 C18 TM1 C18 TM1 C18	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 1300 130 95 133 130 95 133 95 95 95 95 95 95	W 61.4° W W 61.4° W 61.4° W 73.3° W 60.4° W 73.3° W 60.4° W 60.4° W 60.4° W 60.4° W 60.4° W 60.4° W 63.4°	C 167 Mi C C C C C C C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroe-L N nroe-L L nroe-L L nroe-L L nroe-L N nroe-L A nroe-L A nroe-L A nroe-L A esier C esier C	2 G0 M0 2 G0 M0 40 40 41 11 11 11 11 10 11 11 10 10 1	65 65 65 65 65 65 65 65 65 65 65 65 65 6
ore 2 Duo ore 2	E4400 E4300 E2220 E2220 E2160 E2140 E1400 E1400 E1400 430 430 430 430 965 955 960 955 960 950 950 950 950 950 950 950 950 950 95	2×1800 MHz 2×2400 MHz 2×2000 MHz 2×2000 MHz 2×1800 MHz 2×1600 MHz 2×1600 MHz 2×1600 MHz 2×3600 MHz	9x 12x 11x 10x 9x 8x 10x 9x 8x 10x 9x 11x 11x 10x 9x 8x 11x 11x	200 MHz QDI 200 MHz QDI 266 MHz QDI 266 MHz QDI 200 MHz QDI	R 2x32+32/1x2048 KB 2x32+32/1x1024 KB 2x32+32/1x1024 KB 2x32+32/1x1024 KB 2x32+32/1x1024 KB 2x32+32/1x1024 KB 2x32+32/1x1024 KB 2x32+32/121 KB R 32+32/121 KB R 32+32/121 KB R 32+32/121 KB R 12kµ0p+16/2048 KB	1.32 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.31 V 1.375 V 1.3875 V 1.3875 V 1.3875 V 1.30 V 1.30 V 1.31 V	MMX SSE MMX SSE	SE2 SE3 SSE3 S	NX EM64T NX EM64T G4T VT HT G4T VT G4T V	TM2 C1E TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM2 TM1 TM1 C1E	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 130 130 95 130 95 133 95 95 95 95 95 95 95	W 61.4* W W W 61.4* W 61.4* W 61.4* W 73.3* W 73.3* W 60.4* W 60.4* W 60.4* W 60.4* W 60.4* W 63.4* W 63.4* W 63.4*	C 167 Mi C 2 C 2 C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroc-L N nroc-L L nroc-L L nroc-L L nroc-L N nroc-L N nroc-L A nroc-L A nroc-L A seler C esler C esl	2 G0 M0 2 G0 M0 40 40 41 41 41 41 41 41 41 41 41 41	65 65 65 65 65 65 65 65 65 65 65 65 65 6
ore 2 Duo ore 2 Duo ore 2 Duo entium Dual Core entium Dual Core entium Dual Core entium Dual Core eleron eleron eleron eleron eleron entium E entium D entium D	E4400 E4300 E2220 E2200 E2180 E2160 E1400 E1400 E1200 440 430 420 965 960 955 950 950 950 955 950 955 935 935 935 935 935 935 935 935 935	2×1800 MHz 2×2400 MHz 2×2000 MHz 2×2000 MHz 2×1800 MHz 2×1600 MHz 2×1600 MHz 2×000 MHz 1000 MHz 2×3600 MHz 2×3600 MHz 2×3600 MHz 2×3600 MHz 2×3600 MHz 2×3600 MHz 2×3000 MHz 2×3000 MHz 2×3000 MHz 2×3000 MHz 2×3000 MHz 2×3000 MHz 2×3000 MHz 2×8000 MHz 2×8000 MHz	9x 12x 11x 10x 9x 8x 10x 9x 8x 10x 9x 8x 10x 9x 9x 8x 10x 18x 17x 16x 16x 16x 16x 16x 15x 14x 14x 14x 17x	200 MHz QDI 200 MHz QDI 266 MHz QDI 266 MHz QDI 200 MHz QDI	R 2x32+32/1x2048 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/12 KB R 32+32/12 KB R 32+32/12 KB R 32+32/12 KB R 12kµ0p1+16/2048 KB	$\begin{array}{c} 1.32 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.375 \lor \\ 1.375 \lor \\ 1.375 \lor \\ 1.30 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.$	MMX SSE MMX SSE	SE2 2 SE3 SE3 SE2 SE3 SSE3 SE2 SE3 NE SE3 SE3 NE SE	NX EM64T G4T VT	TM2 C1E TM2 TM2 TM2 TM2 TM1 C1E TM1 C1E <td< td=""><td>EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 35 35 35 35 35 35 35 35 35</td><td>W 61.4* W W W 61.4* W 61.4* W 61.4* W 61.4* W 73.3* W 60.4* W 60.4* W 60.4* W 63.4* W 63.2* W 63.2*</td><td>C 167 Mi C 167 Mi C C C C C 376 Mi C 376 Mi</td><td>Co Co Co Co Co Co Co Co Co Co Co Co Co C</td><td>nroe-L N nroe-L L nroe-L L nroe-L L nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A seler C esler C e</td><td>2 G0 M0 2 G0 M0 40 40 41 41 41 41 41 41 41 41 41 41</td><td>65 65 65 65 65 65 65 65 65 65 65 65 65 6</td></td<>	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 35 35 35 35 35 35 35 35 35	W 61.4* W W W 61.4* W 61.4* W 61.4* W 61.4* W 73.3* W 60.4* W 60.4* W 60.4* W 63.4* W 63.2* W 63.2*	C 167 Mi C 167 Mi C C C C C 376 Mi C 376 Mi	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroe-L N nroe-L L nroe-L L nroe-L L nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A nroe-L A seler C esler C e	2 G0 M0 2 G0 M0 40 40 41 41 41 41 41 41 41 41 41 41	65 65 65 65 65 65 65 65 65 65 65 65 65 6
or 2 Duo ore 2 Duo ore 2 Duo ere 2 Duo ere 2 Duo ere 2 Duo entium Dual Core entium Dual Core entium Dual Core eleron eleron eleron eleron eleron eleron eleron entium E entium D entium M entium A "E" entium 4 "E"	E4400 E4300 E2220 E2220 E2160 E2160 E2140 E1400 E1400 e1400 965 955 960 960 950 960 950 940 945 940 945 940 935 930 935 930 935 930 935 925 920 915 661	2x 1800 MHz 2x 2400 MHz 2x 2000 MHz 2x 2000 MHz 2x 1800 MHz 2x 1600 MHz 2x 1600 MHz 2x 1600 MHz 2x 3724 MHz 2x 3600 MHz 2x 3744 MHz 2x 3600 MHz 2x 3600 MHz 2x 3600 MHz 2x 3600 MHz 2x 3000 MHz 3x 3000 MHz	9x 12x 11x 10x 9x 8x 10x 8x 10x 8x 10x 9x 10x 8x 10x 8x 10x 8x 10x 8x 16x 16x 16x 16x 15x 15x 14x 14x 18x	200 MHz QDI 200 MHz QDI	R 2x32+32/1x2048 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/1x1024 KB R 2x32+32/12 KB R 2x32+32/12 KB R 32+32/12 KB R 12kµOp+16/2048 KB	$\begin{array}{c} 1.32 \lor \\ 1.31 \lor \\ 1.375 \lor \\ 1.375 \lor \\ 1.375 \lor \\ 1.30 \lor \\ 1.375 \lor \\ 1.30 \lor \\ 1.30 \lor \\ 1.30 \lor \\ 1.31 \lor \\ 1.30 \lor \\ 1.31 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.40 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.40 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.40 \lor \\ 1.31 \lor \\ 1.32 \lor \\ 1.40 \lor \\ 1$	MMX SSE MMX SSE	SE2 SE3 SSE3 SSE3	NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T NX EM64T Edit VT 64T VT 6	TM2 C1E TM2 TM1 C1E	EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 EIST 65 65 35 35 35 35 35 35 35 35 35 35 35 35 35	W 61.4* W W W 61.4* W 61.4* W 61.4* W 61.4* W 73.5* W 60.4* W 60.4* W 60.4* W 60.4* W 66.6* W 66.6* W 63.4* W 63.2* W 63.2* W 63.2* W 63.2* W 63.2* W 63.4* W 63.4* W 63.4* W	C 167 Mi C 167 Mi C 2 C 2 C 376 Mi C 37	Co Co Co Co Co Co Co Co Co Co Co Co Co C	nroe-L N hroo-L L hroo-L L hroo-L L hroo-L A hroo-L A hroo-L A hroo-L A hroo-L A hroo-L A seler C esler C esle	2 G0 M0 2 G0 M0 40 40 40 41 11 11 11 11 11 11 11 11 11	65 65 65 65 65 65 65 65 65 65 65 65 65 6

CPU Queen

CPU	CPU Clock	Motherboard	Chipset	Memory	CL-RCD-RP-RAS	Score
4x Core i7 Extreme 965 HT	3333 MHz	Asus P6T Deluxe	X58	Triple DDR3-1333	9-9-9-24 CR1	30786
8x Xeon E5462	2800 MHz	Intel S5400SF	i5400	Quad DDR2-640FB	5-5-5-15	30472
4x Core 2 Extreme QX9650	3000 MHz	Gigabyte GA-EP35C-DS3R	P35	Dual DDR3-1066	8-8-8-20 CR2	21421
8x Xeon L5320	1866 MHz	Intel S5000VCL	i5000V	Dual DDR2-533FB	4-4-4-12	20452
4x Core 2 Extreme QX6700	2666 MHz	Intel D975XBX2	i975X	Dual DDR2-667	5-5-5-15	19166
4x Phenom II X4 Black 940	3000 MHz	Asus M3N78-EM	GeForce8300 Int.	Ganged Dual DDR2-800	5-5-5-18 CR2	18636
4x Xeon 5140	2333 MHz	Intel S5000VSA	i5000V	Dual DDR2-667FB	5-5-5-15	16729
8x Opteron HE 2344	1700 MHz	Tyan Thunder n3600R	nForcePro-3600	Unganged Dual DDR2-667R	5-5-5-15 CR1	16146
4x Phenom X4 9500	2200 MHz	Asus M3A	AMD770	Ganged Dual DDR2-800	5-5-5-18 CR2	13693
2x Core 2 Duo E6700	2666 MHz	Abit AB9	P965	Dual DDR2-800	5-5-5-18 CR2	11406
2x Athlon64 X2 Black 6400+	3200 MHz	MSI K9N SLI Platinum	nForce570SLI	Dual DDR2-800	4-4-4-11 CR1	11169
2x Core 2 Duo P8400	2266 MHz	MSI MegaBook PR201	GM45 Int.	Dual DDR2-667	5-5-5-15	9578
2x Pentium T3400	2166 MHz	Toshiba Satellite L305	GL40 Int.	Dual DDR2-667	5-5-5-13	9145
2x Core Duo T2500	2000 MHz	Asus N4L-VM DH	i945GT Int.	Dual DDR2-667	5-5-5-15	7793
2x Core 2 Duo T5600	1833 MHz	Asus F3000Jc Notebook	i945PM	Dual DDR2-667	5-5-5-15	7717
2x Athlon64 X2 4000+	2100 MHz	ASRock ALiveNF7G-HDready	nForce7050-630a Int.	Dual DDR2-700	5-5-5-18 CR2	7280
2x Pentium EE 955 HT	3466 MHz	Intel D955XBK	i955X	Dual DDR2-667	4-4-4-11	7098
2x Xeon	3066 MHz	Asus PCH-DL	i875P + PAT	Dual DDR333	2-2-2-5	6188
2x Opteron 240	1400 MHz	MSI K8D Master3-133 FS	AMD8100	Dual DDR400R	3-4-4-8 CR1	4863
2x PIII-S	1266 MHz	MSI Pro266TD Master-LR	ApolloPro266TD	DDR266 SDRAM	2-3-3-6 CR2	4857
P4EE HT	3733 MHz	Intel SE7230NH1LX	iE7230	Dual DDR2-667	5-5-5-15	4210
Opteron 248	2200 MHz	MSI K8T Master1-FAR	К8Т800	Dual DDR266R	2-3-3-6 CR1	3851
Atom 230	1600 MHz	Intel D945GCLF	i945GC	DDR2-533 SDRAM	4-4-4-12	3779

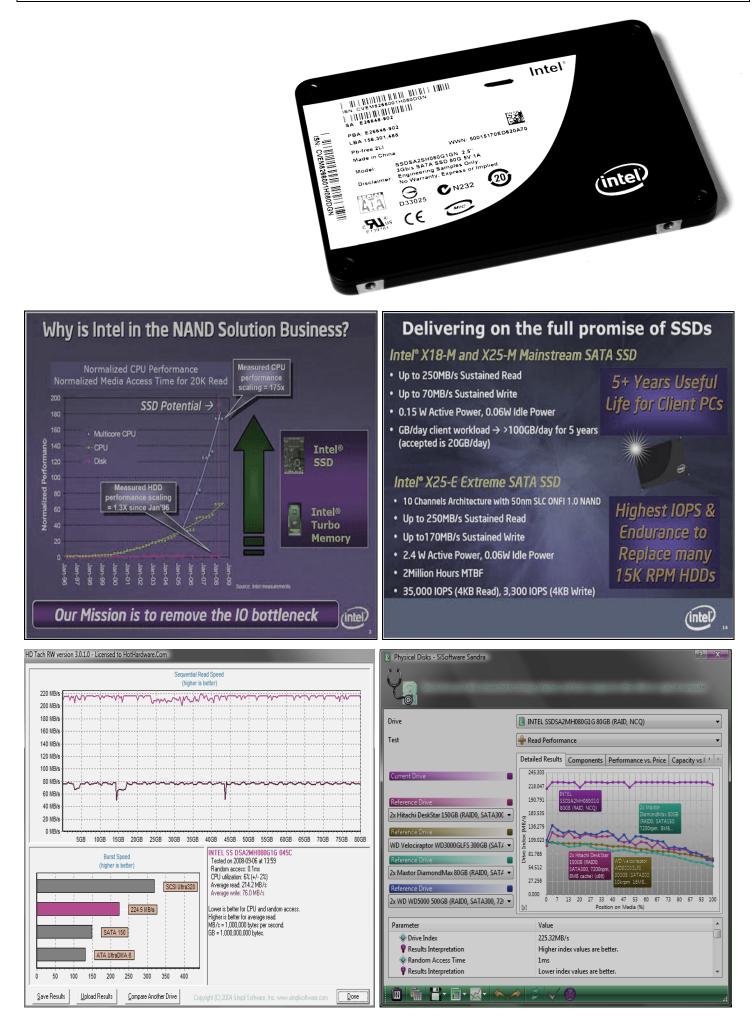
CPU AES

CPU	CPU Clock	Motherboard	Chipset	Memory	CL-RCD-RP-RAS	Score
8x Xeon E5462	2800 MHz	Intel S5400SF	i5400	Quad DDR2-640FB	5-5-5-15	41625
8x Xeon L5320	1866 MHz	Intel S5000VCL	i5000V	Dual DDR2-533FB	4-4-4-12	27698
4x Core i7 Extreme 965 HT	3333 MHz	Asus P6T Deluxe	X58	Triple DDR3-1333	9-9-9-24 CR1	26703
8x Opteron HE 2344	1700 MHz	Tyan Thunder n3600R	nForcePro-3600	Unganged Dual DDR2-667R	5-5-5-15 CR1	22599
4x Core 2 Extreme QX9650	3000 MHz	Gigabyte GA-EP35C-DS3R	P35	Dual DDR3-1066	8-8-8-20 CR2	22435
4x Phenom II X4 Black 940	3000 MHz	Asus M3N78-EM	GeForce8300 Int.	Ganged Dual DDR2-800	5-5-5-18 CR2	21658
4x Core 2 Extreme QX6700	2666 MHz	Intel D975XBX2	i975X	Dual DDR2-667	5-5-5-15	19896
C7	1500 MHz	VIA EPIA EN	CN700 Int.	DDR2-533 SDRAM	4-4-4-12 CR2	17358
4x Xeon 5140	2333 MHz	Intel S5000VSA	i5000V	Dual DDR2-667FB	5-5-5-15	17320
4x Phenom X4 9500	2200 MHz	Asus M3A	AMD770	Ganged Dual DDR2-800	5-5-5-18 CR2	14802
2x Core 2 Duo E6700	2666 MHz	Abit AB9	P965	Dual DDR2-800	5-5-5-18 CR2	9969
2x Pentium EE 955 HT	3466 MHz	Intel D955XBK	i955X	Dual DDR2-667	4-4-4-11	8970
2x Core 2 Duo P8400	2266 MHz	MSI MegaBook PR201	GM45 Int.	Dual DDR2-667	5-5-5-15	8443
2x Athlon64 X2 Black 6400+	3200 MHz	MSI K9N SLI Platinum	nForce570SLI	Dual DDR2-800	4-4-4-11 CR1	8339
2x Pentium T3400	2166 MHz	Toshiba Satellite L305	GL40 Int.	Dual DDR2-667	5-5-5-13	7966
2x Core Duo T2500	2000 MHz	Asus N4L-VM DH	i945GT Int.	Dual DDR2-667	5-5-5-15	7109

CPU ZLib

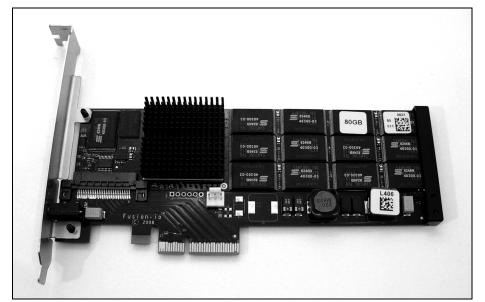
CPU	CPU Clock	Motherboard	Chipset	Memory	CL-RCD-RP-RAS	Score
8x Xeon E5462	2800 MHz	Intel S5400SF	i5400	Quad DDR2-640FB	5-5-5-15	139481 KB/s
4x Core i7 Extreme 965 HT	3333 MHz	Asus P6T Deluxe	X58	Triple DDR3-1333	9-9-9-24 CR1	112330 KB/s
8x Xeon L5320	1866 MHz	Intel S5000VCL	i5000V	Dual DDR2-533FB	4-4-4-12	95887 KB/s
8x Opteron HE 2344	1700 MHz	Tyan Thunder n3600R	nForcePro-3600	Unganged Dual DDR2-667R	5-5-5-15 CR1	88845 KB/s
4x Phenom II X4 Black 940	3000 MHz	Asus M3N78-EM	GeForce8300 Int.	Ganged Dual DDR2-800	5-5-5-18 CR2	80081 KB/s
4x Core 2 Extreme QX9650	3000 MHz	Gigabyte GA-EP35C-DS3R	P35	Dual DDR3-1066	8-8-8-20 CR2	77167 KB/s
4x Core 2 Extreme QX6700	2666 MHz	Intel D975XBX2	i975X	Dual DDR2-667	5-5-5-15	69756 KB/s
4x Xeon 5140	2333 MHz	Intel S5000VSA	i5000V	Dual DDR2-667FB	5-5-5-15	60995 KB/s
4x Phenom X4 9500	2200 MHz	Asus M3A	AMD770	Ganged Dual DDR2-800	5-5-5-18 CR2	58396 KB/s
2x Athlon64 X2 Black 6400+	3200 MHz	MSI K9N SLI Platinum	nForce570SLI	Dual DDR2-800	4-4-4-11 CR1	38059 KB/s
2x Core 2 Duo E6700	2666 MHz	Abit AB9	P965	Dual DDR2-800	5-5-5-18 CR2	35355 KB/s
2x Pentium EE 955 HT	3466 MHz	Intel D955XBK	i955X	Dual DDR2-667	4-4-4-11	29844 KB/s
2x Core 2 Duo P8400	2266 MHz	MSI MegaBook PR201	GM45 Int.	Dual DDR2-667	5-5-5-15	29223 KB/s
2x Pentium T3400	2166 MHz	Toshiba Satellite L305	GL40 Int.	Dual DDR2-667	5-5-5-13	28519 KB/s
2x Xeon	3066 MHz	Asus PCH-DL	i875P + PAT	Dual DDR333	2-2-2-5	25040 KB/s
2x Core 2 Duo T5600	1833 MHz	Asus F3000Jc Notebook	i945PM	Dual DDR2-667	5-5-5-15	23969 KB/s
2x Athlon64 X2 4000+	2100 MHz	ASRock ALiveNF7G-HDready	nForce7050-630a Int.	Dual DDR2-700	5-5-5-18 CR2	23852 KB/s

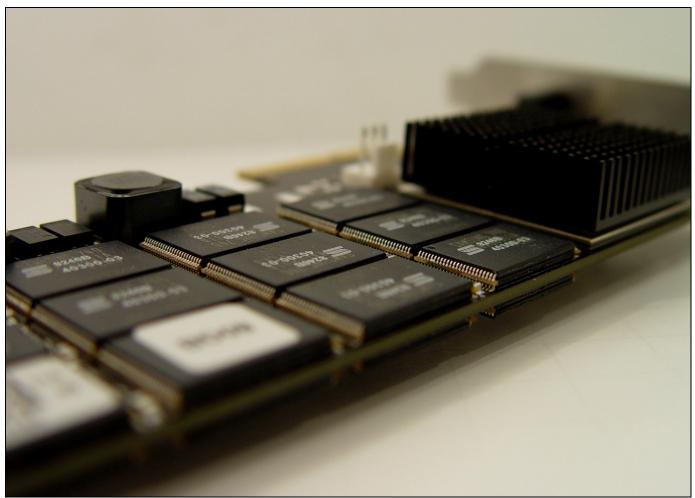
Intel claims 230MB/s read, (benchmark tools say 214MB/s read) - already on market:



Fusion ioDrive SPECIFICATIONS:

NAND Type:Single Level Cell (SLC)Read Bandwidth:700 MB/s (random 16K)Access Latency:50µsBus Interface:PCI-Express x4Operating Systems:Microsoft 64-Bit Windows(64-Bit windows XP, Vista, Server 2003 & 2008)





* * *

With the **ioDrive Duo**, it is now possible for application, database and system administrators to get previously unheard-of levels of performance, protection and capacity utilization from a single server. Performance for multiple **ioDrive Duos** scales linearly, allowing any enterprise to scale performance to **six gigabytes per-second** (Gbytes/sec) of read bandwidth and over 500,000 read IOPS by using just four **ioDrive Duos**.



Fusion-io's Solid State Storage – A New Standard for Enterprise-Class Reliability



Fusion-io's Solid State Storage – A New Standard for Enterprise-Class Reliability

Fusion-io offers solid state storage solutions based on NAND flash that provide a level of integrity and availability for mission-critical data that exceeds today's solid state storage solutions and significantly surpasses that of enterprise-class rotating magnetic storage devices.

With throughput and seek times many times faster than the fastest disk arrays, it is little wonder that enterprise data centers have been keen to include NAND flash as part of their server infrastructure. The primary reason NAND flash has not been widely adopted in the computer industry is its reputation for unreliability. There is a long-standing view that NAND flash storage works well for non-mission-critical applications, such as media storage devices (where the occasional bit error generally translates into a slight audio hiss or a stray errant pixel in a video), but cannot be relied upon for applications where a bit error could crash an operating system or compromise the integrity of critical data.

System architects face a number of storage-related challenges and NAND flash technology presents its own set of unique problems. But Fusion-io has developed patent-pending techniques to create NAND flash-based storage with reliability equal to or exceeding that of disk-based storage. This paper describes several inventions and advancements Fusion-io has introduced to ensure data is not corrupted or lost. Additionally, this paper discusses the probability of catastrophic storage device failure and how Fusion-io's architecture ensures predicable, controlled management of early device failure, long-term device attrition and data changes due to external and data transport interference.

NAND Flash

Flash memory chips are a non-volatile storage medium (i.e., they can retain their information even in the absence of power). The most common types of flash chips are silicon-based NOR and NAND, named after the types of logic gates used in their design. NAND flash, introduced in 1989, has become the most commonly used type of flash chip, due to its quicker write speed. Flash memory continues to grow in popularity as its price steadily declines, its storage capacity increases, and its physical size continues to decrease.

In Fusion-io's storage devices, NAND flash chips are stacked several at a time (to increase density), operated in parallel (to increase throughput) and mounted on a printed circuit board (PCB) that plugs into a PCI-Express (PCIe) slot on the server or in the CPU. The flash media is integrated with the controller onto a single PCI-Express card.

NAND flash, as a storage medium, offers a number of benefits in comparison to rotating magnetic storage devices (aka HDD, Hard Disk Drives). NAND flash has no moving parts and is therefore significantly less prone to shock or movement disturbance. It is a high speed solution in both latency and throughput. Temperature and humidity resistance mean that it can operate in a number of different environments. Finally, NAND flash consumes significantly less power than rotating magnetic storage devices, particularly when you take into account secondary power requirements for device cooling.



However, NAND flash does introduce a number of potential failure points including:

- · Media Media failures can occur on the NAND flash chips themselves.
- Transport Transport errors can occur anywhere along the path carrying data from the CPU through to the NAND flash chips.
- Management There is a small chance that management problems can occur within the logic of the device itself. The code that controls the operation can contain technical problems that can result in data failures.
- · External External problems can affect any part of the process.
- Device Failure Catastrophic hardware failure can also occur. This includes the possibility of internal short circuits and open circuits within the memory array itself.

Protecting the Data

Implementing a variety of design and architectural strategies for protecting data integrity, Fusion-io's NAND flash devices greatly exceed the reliability of rotating magnetic media storage devices, while providing performance that is orders of magnitude better. Fusion-io protects your data at every step, ensuring that nothing is lost or corrupted in transit or on the media.

Data Integrity

Data integrity means having a high degree of confidence that what you put into a storage system is exactly what you get out when you request that data and it is the most important function of a storage system. While being moved from a computer's RAM or CPU to the Fusion-io device, several proven industry-standard approaches are used to ensure data integrity. The CPU, chipset, and RAM use SECDED (Single Error Correct Double Error Detect) or chipkill (method for on-the-fly replacement of a failed chip) to ensure accuracy. Once data is written to the storage medium, it is again checked for accuracy.

When data is read from the storage medium, error correction techniques are again employed to ensure that the data being retrieved is correct. The device can correct a substantial portion of the data being read. NAND's reputation for unreliability is based on studies that show potential data loss without utilizing error correction – or less correction than that employed by the Fusion-io device. Using the methods described here, Fusion-io devices can produce results that exceed target error probability by about four times. Fusion-io's devices also use a patent-pending approach when writing data, which allows the data's path to be reconstructed from information generated during the write process.

Data Availability

Data availability means having a high degree of confidence that data stored will not be lost, either while in transition to the storage device or after it has been written to the media.

Fusion-io employs a wide variety of techniques to overcome some of the common problems associated with data availability in general, and also addresses some that are particular to NAND flash as a storage medium. Generally speaking, NAND flash is substantially more reliable than rotating magnetic media. It eliminates the chance of mechanical failure



(the failure associated with moving parts). There is, however, a chance of bad chips and chip wear-out. Fusion-io mitigates this risk using a variety of approaches.

Fusion-io's redundant, patent-pending approach to writing data allows data to be rebuilt at a very high rate of speed, ensuring rapid data availability. Data is also regularly moved and checked for accuracy to ensure that it does not deteriorate on the flash chip. This also consolidates good data and reallocates space on the drive to ensure greater data availability. This system also spreads data evenly across the device, ensuring uniform wear across all chips.

Additionally, Fusion-io uses multiple error correction code (ECC) techniques to identify and correct faulty data. Using ECCs, the device controller can correct up to 11 missing or incorrect bits out of every 240 bytes. One of the biggest benefits of ECC routines is that it they allow the device to predict the likelihood of failure on individual chips. When a particular area of a chip has passed a set unreliability threshold, its data can be moved and that are will be taken out of service. The controller continues to identify and remove bad blocks, regions of chips or even entire chips so that ordinary wear-out does not cause catastrophic failure rather a very predictable wear-out.

Device Longevity

The majority of this paper has concentrated on NAND flash in an enterprise-class storage device, and how to leverage its strengths while overcoming its weaknesses. NAND flash, however, is only part of a Fusion-io's storage device. The flash chips reside on a PCIe adapter card that has a number of other parts as well, all of which are susceptible to failure. The life of a NAND flash storage device can be estimated by examining the failure rate of its component parts. Wear-out is generally a function of having lost enough storage cells that both capacity and reliability drop below acceptable thresholds. This can be assessed by evaluating and keeping a record of the amount of errors detected at each physical location.

NAND flash wears out at a predictable rate as described by the formulas below. Effective use of wear-leveling strategies employed by Fusion-io can significantly improve the life expectancy of its drives. Please note that the formulas are applied to both MLC and SLC NAND-based non-volatile memory technologies. Single-Level Cell (SLC) NAND and Multi-Level Cell (MLC) NAND offer capabilities that serve two very different types of applications – respectively, those requiring high performance at an attractive cost-per-bit and those seeking even higher performance over time, that are less cost-sensitive:

Average-lifetime = lifetime / read-write- ratio

TYPE / WRITE DUTY	AVERAGE ESTIMATED LIFETIME FORMULA
SLC flash @ 40% write duty	25 calendar years
MLC flash @ 20% write duty	10 calendar years
MLC flash @ 40% write duty	5 calendar years



Average estimated lifetime based on Fusion-io lab testing

The read/write ratio is difficult to predict, and will vary considerably from environment to environment. As a point of reference, the International Disk-drive Equipment and Materials Association (IDEMA), an industry trade group that publishes storage device standards, recommends a read/write ratio of 60%/40% for its server-class device reliability testing (IDEMA Standards, Document R3-98).

Flashback Protection

Enterprises have long sought to take advantage of the speed, size, low-power and high-performance of NAND Flash because of its potential to change the way they manage large amounts of active data. The primary objection to NAND flash has been the reliability of the medium. Fusion-io has eliminated this barrier by inventing a revolutionary self-healing technology, known as Flashback Protection, in our controllers that instantaneously restores, corrects and resurrects lost data in the flash-based storage sub-system. Flashback Protection is accomplished by collectively using advanced bit error correction, proactive data integrity monitoring of stored data and the recent addition of a dedicated chip to repair failed devices.

Fusion-io is the first and only company to bring RAID-class redundancy and reliability using Flashback Protection down to the card level. The Flashback Protection system allows users to diagnose and correct system errors. Fusion-io integrates dedicated NAND flash chips, which offer information that enables the detection of single bit errors. This technique eliminates data loss due to chip failures and extends the usable lifetime of the NAND flash-based storage device. The NAND flash chips on Fusion-io's products contain an innovative storage architecture that enable it to deliver the performance, and now the reliability, of a storage area network (SAN) at a fraction of the power, size and cost of traditional disk arrays.

Controlled Predictable Usage Versus Catastrophic Failure

Among the greatest reliability benefits of the Fusion-io storage device is its ability to:

- · Restore and Protect data
- · Monitor and predict media wear-out
- · Correct bad data as necessary
- · Take blocks out of service when their failure rate becomes unacceptable
- · Replace bad chips on-the-fly
- · Move the data to a known good location (and update corresponding mapping information)

Data stored on the Fusion-io medium is double protected using both ECCs and parity data on the redundant chip. The net effect is that wear-out of the device, instead of being catastrophic, is predictable and incremental.



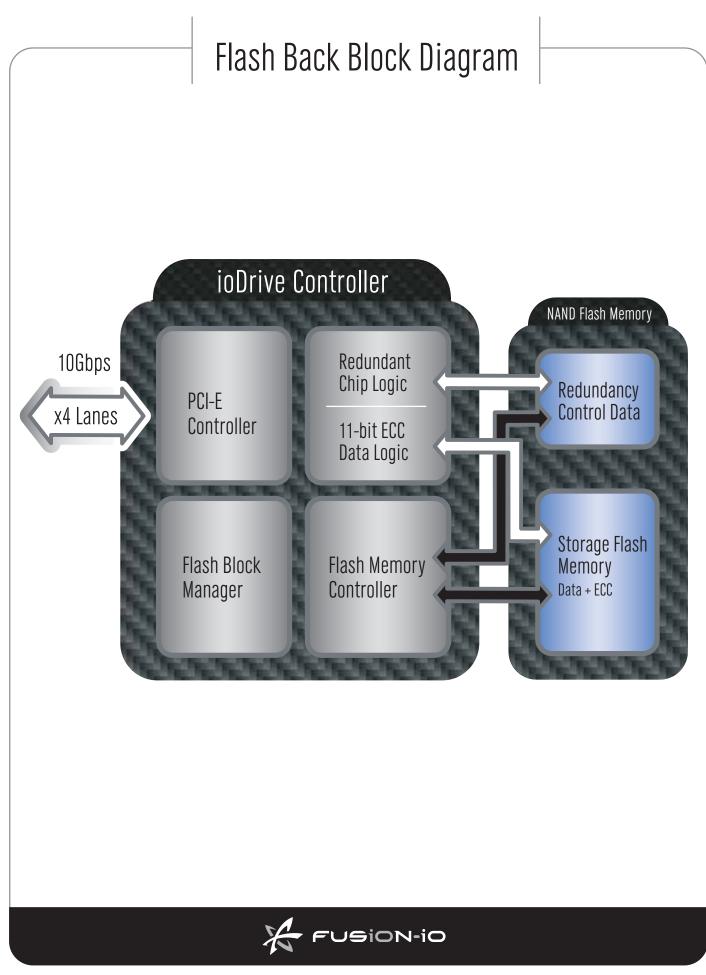
A Fusion-io device provides advanced warning prior to wear-out. Fusion-io supports today's monitoring management functions to measure and report on the device's status and usable life. In almost all cases, device upgrade is a smooth and predictable process, rather than an emergency situation.

Fusion-io protects your data at every stage of its path from your applications to the NAND flash storage medium, ensuring that nothing is lost or corrupted along the way or while the data is being stored. Data is checked multiple times, using several error detection methods. Once it reaches the storage medium, it is stored with robust error correction encoding that lets the flash device not only identify but correct bit errors. Fusion-io's data integrity design target is a 1 in 10³⁰ probability of undetected bad data and a 1 in 10²⁰ probability of uncorrectable data, as compared to a 1 in 10¹⁶ probability of undetected or uncorrectable errors for rotating magnetic storage devices.

Conclusion

Now with Fusion-io's comprehensive approach to data integrity, it is safe to exploit the exponential performance gains and many other benefits offered by NAND flash storage. The storage architecture pioneered by Fusion-io ensures predictable, controlled mitigation of early device failure, long-term device attrition and data changes due to external and data transport interference—issues that have up to now limited the adoption of NAND flash-based storage at the enterprise level. Fusion-io's NAND flash devices exceed the reliability of rotating magnetic media storage devices while providing an order of magnitude performance improvement.





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Fusion-io Announces the ioDrive Duo—The World's Fastest and Most Innovative SSD

PCI Express, server-based solid-state storage offering sets a new standard for enterprise application-centric storage, with up to 640 gigabytes of capacity and 1.5 gigabytes per-second of sustained throughput

SALT LAKE CITY - March 11, 2009 - Fusion-io, the leader in solid-state architecture and highperformance I/O solutions, today announced the ioDrive Duo, which doubles the slot capacity of Fusion-io's successful PCI Express-based ioDrive storage solution. The new ioDrive Duo is the market's fastest and most innovative server-based solid-state storage solution.

With the ioDrive Duo, it is now possible for application, database and system administrators to get previously unheard-of levels of performance, protection and capacity utilization from a single server. Performance for multiple ioDrive Duos scales linearly, allowing any enterprise to scale performance to six gigabytes per-second (Gbytes/sec) of read bandwidth and over 500,000 read IOPS by using just four ioDrive Duos.

"Many database and system administrators are finding that SANs are too expensive and don't meet performance, protection and capacity utilization expectations," said David Flynn, CTO of Fusion-io. "This is why more and more application vendors are moving toward application-centric solid-state storage. The ioDrive Duo offers the enterprise the advantages of application-centric storage without application-specific programming."

ioDrive Duo Product Details

The following specifications describe the physical and performance characteristics of the ioDrive Duo.

Performance

Based on PCI Express x8 or PCI Express 2.0 x4 standards, which can sustain up to 20 gigabits per-second of raw throughput, the ioDrive Duo has more than enough bandwidth to obtain indus-try-leading performance from a single card. The ioDrive Duo can easily sustain 1.5 Gbytes/sec of read bandwidth and nearly 200,000 read IOPS. Its performance metrics are as follows:

- Sustained read bandwidth: 1500 MB/sec (32k packet size)
- Sustained write bandwidth: 1400 MB/sec (32k packet size)
- Read IOPS: 186,000 (4k packet size)
- Write IOPS: 167,000 (4k packet size)
- Latency $< 50 \mu sec$



Reliability

The ioDrive Duo offers unmatched solid-state protection for data integrity and reliability with triple redundancy for a single storage component.

- Multi-bit error detection and correction
- Patent-pending Flashback protection, offering chip-level N+1 redundancy and on-board self-healing so that no servicing is required
- Optional RAID-1 mirroring between two ioMemory modules on the same ioDrive Duo, offering complete redundancy on a single PCIe card

Capacity

The ioDrive Duo comes in the following capacities:

- 160 Gbytes
- 320 Gbytes
- 640 Gbytes
- 1.28 TB (second half of 2009)

The ioDrive Duo will be available in April 2009. To find out more about how this and Fusion-io's other enterprise solid-state storage products can benefit your organization, please visit <u>www.fusionio.com.</u>

About Fusion-io

Fusion-io is a leading provider of enterprise solid-state technology and high-performance I/O solutions. The company's solid-state storage technology closes the gap between processing power and storage needs delivering breakthrough performance at a fraction of the cost of traditional disk-based storage systems. The result is a world of possibilities for performance-starved applications.





- > Sustain over a GB/sec of bandwidth
- > Easily RAID multiple ioDrive Duo's
- > OS support for Windows, Linux & Solaris

ioDrive Duo Capacity	160GB	320GB	640GB
NAND Type	Single Level Cell (SLC)	Single Level Cell (SLC)	Multi Level Cell (MLC)
Write Bandwidth	1.1 GB/s (32k packet size)	1.4 GB/s (32k packet size)	1.0 GB/s (32k packet size)
Read Bandwidth	1.5 GB/s (32k packet size)	1.5 GB/s (32k packet size)	1.4 GB/s (32k packet size)
IOPS*	200,832 reads (4k packet size) 132,118 writes 4k packet size)	185,022 reads (4k packet size) 167,784 writes (4k packet size)	126,601 reads (4k packet size) 180,530 writes (4k packet size)
Access Latency	50µs Read	50µs Read	80µs Read
Bus Interface	PCI-Express x8 and PCI Express 2.0 x4	PCI-Express x8 and PCI Express 2.0 x4	PCI-Express x8 and PCI Express 2.0 x4
Weight	Less than 10 ounces	Less than 10 ounces	Less than 10 ounces
Operating Systems	Microsoft Windows**, Open Solaris 10 Solaris 10, RHEL 4 & 5; SLES 9 & 10	Microsoft Windows**, Open Solaris 10 Solaris 10, RHEL 4 & 5; SLES 9 & 10	Microsoft Windows**, Open Solaris 10 Solaris 10, RHEL 4 & 5; SLES 9 & 10
Wear Leveling and Sophisticated ECC (@ 5-TB write-erase / day)	24yrs	48yrs	16yrs

* Performance achieved using multiprocessor enterprise server ** 64-Bit Windows XP, Vista, Server 2003 & 2008

STANDARDS

Form Factor	Full height, 3/4 length PCI Express 2.0
Connectivity	PCI Express electromechanical spec 2.0
Power	PCI Express power spec 2.0

ENVIRONMENTAL SPECIFICATIONS

		Min	Max
Temperature (°C)*	Operational	0	55
	Non-operational	- 40	70
Air Flow (LFM)		300	
Humidity (%)	Non-condensing	5	95
Altitude (ft)	Operational		10,000
	Non-operational		30,000

* Temperature derated 1 C per 1000 ft elevation above sea level 100% Assembled in the U.S.A.

AGENCY

US / Canada	FCC Part 15, ICES-003, Class A
Europe	2004/108/EC EMC Directive CE Mark;
Japan	VCCI, Class A
Taiwan	BSMI, Class A
New Zealand /Australia	AS/NZS 3548 Class A
RoHS	R5 (Directive 2002/95/EC)



FUSION-iO



- > Easily RAID multiple ioDrives together
- > Managed like simple block storage

ioDrive Capacity	80GB	160GB	320GB
NAND Type	Single Level Cell (SLC)	Single Level Cell (SLC)	Multi Level Cell (MLC)
Write Bandwidth	550 MB/s (random 16K)	600 MB/s (random 16K)	500 MB/s (random 8K)
Read Bandwidth	700 MB/s (random 16K)	700 MB/s (random 16K)	700 MB/s (random 32K)
IOPS*	102,000 (random 4k reads) 91,000 (random 4k writes) 88,000 (70/30 random 4k mix)	104,400 (random 4k reads) 103,925 (random 4k writes) 95,000 (70/30 random 4k mix)	60,000 (random 4k reads) 79,000 (random 4k writes) 65,000 (70/30 random 4k mix)
Access Latency	50µs Read	50µs Read	80µs Read
Bus Interface	PCI-Express x4	PCI-Express x4	PCI-Express x4
Weight	Less than 2 ounces	Less than 2 ounces	Less than 2 ounces
Operating Systems	RHEL 4 & 5; SLES 9 & 10 Microsoft 64-Bit Windows**	RHEL 4 & 5; SLES 9 & 10 Microsoft 64-Bit Windows**	RHEL 4 & 5; SLES 9 & 10 Microsoft 64-Bit Windows**
Wear Leveling and Sophisticated ECC @ 5-TB write-erase / day)	24yrs	48yrs	16yrs

* Performance data provided by Medusa Labs. ** 64-Bit Windows XP, Vista, Server 2003 & 2008

STANDARDS

Form Factor	Low profile PCI Express x4 slot (spec 1.1)	
Connectivity	PCI Express x4 (electromechanical spec 1.1)	
Power	PCI Express x4 (power spec 1.1)	

ENVIRONMENTAL SPECIFICATIONS

		Min	Max
Temperature (°C)*	Operational	0	55
	Non-operational	- 40	70
Air Flow (LFM)		300	
Humidity (%)	Non-condensing	5	95
Altitude (ft)	Operational		10,000
	Non-operational		30,000

* Temperature derated 1 C per 1000 ft elevation above sea level

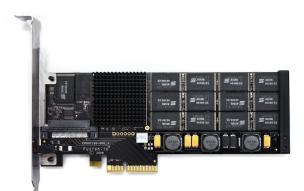
SAFETY

US / Canada	UL60950, CSA C22.2 No.60950-1-03
Europe	TUV EN60950-1:2001; 3N50825-1:

100% Assembled in the U.S.A.



US / Canada	FCC Part 15, ICES-003, Class A
Europe	2004/108/EC EMC Directive CE Mark;
Japan	VCCI, Class A
Taiwan	BSMI, Class A
New Zealand /Australia	AS/NZS 3548 Class A
RoHS	R5 (Directive 2002/95/EC)



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FUSION-iO